BL203 Ethernet/IP Distributed I/O





BL203 User Manual

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Shenzhen Beilai Technology Co.,Ltd

Website: https://www.bliiot.com



Preface

Thanks for choosing BLIIoT Distributed I/O. These operating instructions contain all the information you need for operation of BL203.

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Disclaimer

This document is designed for assisting user to better understand the device. As the described device is under continuous improvement, this manual may be updated or revised from time to time without prior notice. Please follow the instructions in the manual. Any damages caused by wrong operation will be beyond warranty.

Revision History

Update Date	Version	Description	Owner
2021-10-13	V1.0	First Edition	ZLF
		Add Profinet, EtherCAT	
2022-07-01	V1.1	protocol, add platform, logic	HYQ
		control functions	
2023-07-27	V1.1	Change Model name	HYQ
		Add BL203, BL206, BL207	нуо
2023-10-24	VI.Z	description	
2023-10-24	V1.2	User manual split by model	HYQ



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BL203 Ethernet/IP Distributed I/O Coupler



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1 Product Introduction

1.1 Overview

BL203 coupler is a data acquisition and control system based on a powerful 32-bit microprocessor design with Linux operating system and support for Ethernet/IP protocols, allowing quick access to Ethernet/IP-enabled PLCs in the field for IIoT and industrial automation applications.

The BL203 distributed I/O system consists of 3 parts: Coupler, I/O modules and terminal module.



The communication between the node and the field devices (eg PLC) takes place via the Ethernet interface of the fieldbus coupler, and the communication between the fieldbus coupler and the I/O modules takes place via the local bus. The two Ethernet interfaces are internally integrated with a switch function, which can establish a linear topology without the need for additional switches or hubs.

The system needs to use the power module to provide 24VDC system voltage and 24VDC field voltage. Since two independent power supplies are used, the field voltage input interface and system voltage input interface of BL203 coupler are electrically isolated from each other.

When assembling fieldbus node modules, each I/O module can be arranged in any combination, and it is not required to be grouped by module type.



A terminal module must be plugged into the end of a fieldbus node to ensure correct data transmission.

1.2 Typical Application

High reliability, easy expansion, easy setting, and convenient network wiring, these capabilities let users efficiently adapt the BL203 I/O system to a variety of complex industrial applications.

1.3 Features

- > Each I/O system can have a maximum of I/O 32 modules.
- Support Ethernet/IP protocol.
- The field side, the system side and the bus side are electrically isolated from each other.
- Support 2 X RJ45 interface, integrated switch function, can establish line topology, without the need for additional switches or hubs.
- > Convenient wiring connection technology, screw-free installation.

Description	Model	Channel	Туре
Modbus-TCP I/O Coupler	BL200	/	/
Profinet I/O Coupler	BL201	/	/
EtherCAT I/O Coupler	BL202	/	/
Ethernet/IP I/O Coupler	BL203	/	Ethernet/IP
OPC UA EdgelO Controller	BL205	/	/
MQTT EdgelO Controller	BL206	/	/
MQTT+OPC UA+Modbus TCP	BL206Pro	/	/
BACnet/IP I/O Coupler	BL207	/	/
BACnet/IP+MQTT+OPC UA	BL207Pro	/	/
8CH DI	M1081	8	NPN (low level trigger)
8CH DI	M1082	8	PNP (high level trigger)
16CH DI	M1161	16	NPN (low level trigger)
16CH DI	M1162	16	PNP (high level trigger)
4CH DO	M2044	4	Relay

1.4 Model List



8CH DO	M2081	8	PNP
8CH DO	M2082	8	NPN
16CH DO	M2161	16	PNP
16CH DO	M2162	16	NPN
4CH AI Single-Ended	M3041	4	0-20mA/4-20mA
4CH AI Single-Ended	M3043	4	0-5V/0-10V
4CH AI Differential	M3044	4	0-5V/0-10V
4CH AI Differential	M3046	4	±5V/±10V
4CH AO	M4041	4	0-20mA/4-20mA
4CH AO	M4043	4	0-5V/0-10V
4CH AO	M4046	4	±5V/±10V
2CH RTD	M5021	2	3Wire PT100
2CH RTD	M5022	2	3Wire PT1000
2CH RTD	M5023	2	4Wire PT100
2CH RTD	M5024	2	4Wire PT1000
4CH TC	M5048	4	TC(B/E/J/K/N/R/S/T)
2CH RS485	M6021	2	RS485
2CH RS232	M6022	2	RS232
1CH RS485, 1CH RS232	M6023	2	RS485+RS232
Power module	M7011	/	/
Terminal module	M8011	/	/



2 Hardware

2.1 I/O Coupler



2.2 Dimension

Unit:mm





2.3 Data Contacts/Internal Bus

The communication between the fieldbus coupler/controller and the I/O modules, as well as the system power supply of the I/O modules are realized via the internal bus. The internal bus is made up of 6 data contacts, these gold-plated contacts are self-cleaning when connected.





2.4 Power Jumper Contacts

The power module included with the coupler has two self-cleaning power jumper contacts for powering the field side. This power supply has a maximum current of 10A across the contacts, current exceeding the maximum will damage the contacts. When configuring the system, it must be ensured that the above-mentioned maximum current is not exceeded. If it exceeds, a power expansion module needs to be inserted.



No.	Туре	Description
1	Spring contact	Supply 24V to the field side
2	Spring contact	Supply 0V to the field side



2.5 Terminal Point



Name	Description		
24V	System Power 24VDC		
0V	System Power 0VDC		
+	Connections Field Supply 24 VDC		
+	Connections Field Supply 24 VDC		
-	Connections Field Supply 0 VDC		
-	Connections Field Supply 0VDC		
PE	Grounding		
PE	Grounding		

2.6 Factory Reset

This reset button is used to restore the device configuration parameters to the factory state.

Operation steps:

1. When the device is running, open the flip cover;

2.Press and hold the button for more than 5 seconds, until all the LED lights go off, indicates reset successful, and then the device will automatically restart.





2.7 Electrical Schematic



3 Installation

3.1 Installation Sequence

All distributed couplers and I/O modules from Beilai Technology must be mounted on a standard DIN 35 rail.

Starting from the coupler, the I/O modules are assembled from left to right, and the modules are installed next to each other. All I/O modules have grooves and power jumper contacts on the right side, to avoid assembly errors, I/O modules must be inserted from the right and top to avoid damage to the modules.

Utilizes a tongue and groove system to form a secure fit and connection. With the automatic locking function, the individual components are securely fixed on the rail



after installation.

Don't forget to install the terminal module! Always plug a terminal module (eg TERM) into the end of the I/O module to ensure correct data transmission.

3.2 Install Coupler

1. Snap the coupler onto the DIN rail first;

2.Use a tool such as a screwdriver to turn the locking cam until the locking cam engages the DIN rail.



3.3 Remove Coupler

1.Use a screwdriver to turn the locking disc cam until the locking cam no longer engages the rail.



2.Pull the release tab to remove the coupler from the assembly





Data or power contacts are electrically disconnected from adjacent I/O modules when the coupler/controller is removed.

3.4 Insert I/O Modules

1. When inserting the module, make sure the tabs on the module line up with the grooves of the coupler or other I/O module to which it is attached.



2.Press the I/O module into the assembly position until the I/O module snaps into the rail.





After the I/O module is installed, the electrical connection to the coupler (or the previous I/O module) and the following I/O module is established via the data contacts and the power jumper contacts.

3.5 Remove I/O Modules



Pull up on the latch to remove the I/O module from the assembly.

When the I/O module is removed, the electrical connection to the data or power jumper contacts is disconnection.

4 Device Connection

4.1 Wiring

CAGE CLAMP connection is suitable for solid, stranded and fine-stranded conductors. Only one wire can be connected to each CAGE CLAMP. If there is more than one wire, ¹⁵ Shenzhen Beilai Technology Co., Ltd V1.2



it must be merged into a point before being connected.

- 1. Open the CAGE CLAMP by inserting the tool into the opening above the junction.
- 2. Insert the wire into the corresponding open connection terminal.

3. Once the tool is removed, the CAGE CLAMP closes and the wire is clamped firmly by the spring.



4.2 Power Supply

System and field voltages are supplied by power supply modules. The power supply module of the BL203 coupler supplies power for the internal electronics of the coupler and the I/O modules. If necessary (there are many I/O modules and the current is relatively high), it can also be provided through an independent power supply module. The fieldbus interface (Ethernet interface), system and field are galvanically isolated from each other.

4.2.1 System Power

BL203 couplers require 24V DC system power, which is connected from the terminal of the power supply module. The 5V bus voltage required inside the system is converted from the 24V system voltage.

The power supply module only has proper fuse protection, please provide proper overcurrent protection externally.

Please pay attention to matching the output power of the power supply module and the load power to avoid excessive load current.





4.2.2 On-site Power Supply

The power supply module supplies 24 VDC on the field side to power the sensors and actuators.

Field power supply only has proper fuse protection. Without overcurrent protection, electronic equipment can be damaged.



Field-side power is automatically output from the power jumper contact when the I/O module is connected. The continuous load current across the contacts of the power supply must not exceed 10 A.

The problem of excessive load power on the system side or on the field side can be

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solved by plugging in additional power supply modules. After plugging in an additional power supply module, a new voltage potential may appear on the field side. In the case where electrical isolation is not required, the field power supply and the system power supply can use the same power supply.

4.2.3 Grounding

When installing the enclosure cabinet, the cabinet must be grounded, and the rail is electrically connected to the cabinet through screws to ensure that the rail is properly grounded. Grounding can increase resistance to electromagnetic interference. Some components in the I/O system have rail contacts that dissipate EMI onto the rail.



5 BL203 Ethernet/IP Coupler

5.1 BL203 Coupler Overview

The BL203 coupler supports standard Ethernet/IP protocol access. The coupler supports a maximum input of 504 bytes and a maximum output of 504 bytes. The number of extended IO modules supported is 32.



5.2 Technical Parameters

Name	Parameter	Description	
	Input voltage(system)	24 VDC	
	Input current(system)	MAX 500 mA@24VDC	
Svetom	Power Efficiency	84%	
bower	Internal bus voltage	5VDC	
power	Coupler current consumption	MAX 300mA@5VDC	
	I/O current consumption	MAX 1700mA@5VDC	
	Isolation protection	500 V system/supply	
	Input voltage (field)	24 VDC	
Field power	Current carrying capacity (power jumper contacts)	MAX 10 ADC	
	Number	2 X RJ45	
	Transmission medium	Twisted Pair STP 100 Ω Cat 5	
Ethernet	MAX cable length	100m	
Ethernet	Baud rate	10/100 Mbit/s	
		ESD contact: 8KV, Surge:	
	Isolation protection	4KV(10/1000us)	
	Operating system	Linux	
	CPU	300MHz	
	RAM	64MB	
	Flash	128MB	
	I/O Modules	MAX 32	
	Protocol	Ethernet/IP, HTTP, DHCP, DNS	
		The maximum input length is 504	
System	Process data area	bytes, and the maximum output	
		length is 504 bytes	
	Maximum number of explicit	10	
	message connections		
	Maximum number of implicit	5	
	message connections	,	
	Maximum number of CIP	10	
	connections		
Wirina	Method	CAGE CLAMP	
method	Wire diameter	0.08 mm ² 2.5 mm ² , AWG 28	
		14	



	Strip length	8 mm 9 mm / 0.33 in	
	Working temperature	0 55 ° C	
	Storage temperature	-40 ··· 70 ° C	
Environment	Relative humidity	5 ··· 95% no condensation	
	Working altitude	0 ··· 2000 m	
	Protection type	IP20	
	Width	48mm	
Dimension	Length	100mm	
	Height	69mm	
	Color	Light gray	
Matarial	Housing material	Polycarbonate, Nylon 6.6	
Watenai	Fire load	1.239 MJ	
	Weight	180g	
Installation	Method	DIN-35 rail	
		EN 55022: 2006/A1: 2007 (CE	
		&RE) Class B	
		IEC 61000-4-2 (ESD) Level 4	
Certificates	EMC	IEC 61000-4-3 (RS) Level 4	
		IEC 61000-4-4 (EFT) Level 4	
		IEC 61000-4-5 (Surge)Level 3	
		IEC 61000-4-6 (CS)Level 4	
		IEC 61000-4-8 (M/S) Level 4	

5.3 Hardware Interface

5.3.1 LED Indicators

 PWR SYS RUN ERR IO RUN IO ERR 				
LED	Description	Color	Status	Meaning
PWR	Power indicator	Red	ON	Power connection



				successful
			OFF	No power
0.40	O set a sectionality of a se	0	ON	System is abnormal
313	System indicator	Green	OFF	System is running normally
	Pupping indicator	Groop	Flashing	System is running normally
RUN	Running Indicator	Green	OFF	System is abnormal
	Error indicator		ON	Ethernet/IP protocol
ERR		Red		connection error
			OFF	No errors
	I/O Running indicator	Green	Flashing	I/O module is working
I/O RUN				normally
			OFF	Module not inserted
I/O ERR	I/O Error indicator		ON	I/O module communication
		Red		error
			OFF	No errors



LED	Description	Color	Status	Meaning
c	System 24V power	Crean	ON	Power is OK
5	indicator	Green	OFF	No power
F	Field 24) (now or indicator	Croop	ON	Power is OK
	Field 24v power indicator Green		OFF	No power

5.3.2 Ethernet Port

Connect to the Ethernet-based field bus through the ETH 1 interface. ETH2 is used to connect other nodes that need to access the Ethernet.





5.3.3 IP Address Selection Switch

The 8-bit DIP switches are used to set the IP address. The DIP switches are coded starting from DIP switch 1 with the lowest valid bit (2^{0}) to DIP switch 8 with the highest valid bit (2^{7}), which corresponds to decimal values: 0-255.



When the value of DIP switch is 1111 1111 (decimal 255), the IP address is set according to the web page, and the web page setting can specify the IP address or set up the automatic acquisition, and when the web page is not set up, the IP address is 192.168.1.10.

For DIP switch values of 0000 0000 - 1111 1110 (decimal 0-254), the 3rd byte of the IP address is determined, and the 1st, 2nd and 4th bytes are fixed bytes, i.e. 192.168.xxx.253.

5.4 Process Data Definition

BL203 does not support the data collected by the serial port module temporarily, the data point address of the I/O module is determined by the Ethernet/IP master station.

Voltage(0-5V)	Voltage(0-10V)	Decimal	Hexadecimal
5	10	32767	0x7FFF
-	-	•	•
•	-		•
2.5	5	16383	0x3FFF
-	-	•	-
-	-	-	-
0	0	0	0x0000

AO 0-5V/0-10V output value

If analog output 3V is required

When the range is 0-5V, send value 3*32767/5=19660.2, because AO value is an



integer, then send 19660.

When the range is 0-10V, send value 3*32767/10=9830.1, because AO value is an integer, then send 9830.

AO -5-5V/-0-10V output value

Voltage(-5-5V)	Voltage(-10-10V)	Decimal	Hexadecimal
5	10	32767	0x7FFF
•	•		•
2.5	5	16383	0x3FFF
•	-		•
-2.5	-5	-16383	0xC001
			•
-5	-10	-32767	0x8001

If analog output 3V is required

When the range is -5-5V, send value 3*65534/10 = 19660.2, because AO value is an integer, then send 19660.

When the range is -10-10V, send value 3*65534/20 = 9830.1, because AO value is an integer, then send 9830.

Current(0-20mA)	Current(4-20mA)	Decimal	Hexadecimal
20	20	32767	0x7FFF
•	•		•
•	•		•
10	12	16383	0x3FFF
	•		•
•	•		•
0	4	0	0x0000

AO 0-20mA/4-20mA output value

If analog output 17mA is required

When the range is 0-20mA, send value 17 * 32767/20 = 27851.95, because AO value is an integer, then send 27852.

When the range is 4-20mA, send value (17-4) * 32767/16 = 26623.1875, because AO value is an integer, then send 26623.

AI 0-20mA/4-20mA input value

Current(0-20mA)	Current(4-20mA)	Decimal	Hexadecimal
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20	20	32767	0x7FFF
•	-	-	-
	-	-	-
10	12	16383	0x3FFF
			•
	-		•
0	4	0	0x0000

Example: In the master station, the AI display value is 23456, when range is 0-20mA, the AI theoretical value is: 23456/32767*20=14.3168mA. When range is 4-20mA, the AI theoretical value is: 23456/32767*16+4=15.4535mA.

AI 0-5V/0-10V input value

Voltage(0-5V)	Voltage(0-10V)	Decimal	Hexadecimal
5	10	32767	0x7FFF
-			
		•	
2.5	5	16383	0x3FFF
	-		
0	0	0	0x0000

Example: The value of AI displayed in the master station is 23456, then the theoretical value of AI for range 0-5V is 23456/32767*5=3.579V. The theoretical value of AI for range 0-10V is 23456/32767*10=7.158V.

Voltage(0-5V)	Voltage(0-10V)	Decimal	Hexadecimal
5	10	32767	0x7FFF
•	-	-	-
2.5	5	16383	0x3FFF
			-
-2.5	-5	-16383	0xC001
	-	-	-
-5	-10	-32767	0x8001

AI -5-5V/-10-10V input value

Example: The value of AI displayed in the master is 23456, then the theoretical value of AI for range -5-5V is 23456/65534*10=3.579 V. For range -10-10V, the theoretical



value of AI is 23456/65534*20=7.158V.

5.5 Coupler Connection

The BL203 coupler is used as a Ethernet/IP slave, and both ETH1 and ETH2 network ports can be directly connected to the Ethernet/IP master, or connected to the Ethernet/IP master through a switch.



5.6 BL203 Web Configuration

The web configuration for the BL203 Ethernet/IP Coupler is primarily designed to view the Byte occupied by the IO modules, the Byte length size of T-->O and O-->T.

5.6.1 Preparation Before Configuration

To successfully access the BL203 coupler, it must be properly installed and connected to the computer. In addition, configure them with correct IP addresses to keep them in the same network segment.



5.6.1.1 Connect Computer and Coupler

1.Mount the fieldbus node on a DIN35 rail. Follow the installation instructions in the "Installation" chapter.

2.Connect the 24 V power supply to the system power terminals.

3. The computer and the bus node can be connected in two ways, one is that the two are connected to the switch device of the local area network through the Ethernet port; the other is that the two are directly connected point-to-point. For detailed steps, follow the instructions in the "Coupler Connection" chapter.

4. Turn on the power supply and start supplying power.

The coupler is initialized after power-up, creates process image according to the I/O modules configuration of the fieldbus node.

5.6.1.2 Configure Computer IP Address

There are two ways to configure PC IP address. One is to turn on the automatic IP address option on the PC's local connection to dynamically assign DHCP in the network. The other is to configure a static IP address with the coupler node on the same network segment on the local connection of the PC.

Takes Windows 7 system as an example for configuration. Windows systems are all configured similarly.

1.Click Start > Control Panel > Network and Sharing Center, and click local connection in the window that opens.





2.In the local connection status window, click Properties.

Ngemeen		
Verbinding		
IPv4-verbindir	ngsmogelijkhede <mark>n:</mark>	Internet
IPv6-verbindir	ngsmogelijkheden:	Geen internettoegang
Status van me	dia:	Ingeschakeld
Tijdsduur:		00:11:18
Snelheid:		100,0 Mbps
Snelheid: Details Activiteit]	100,0 Mbps
Snelheid: Details Activiteit	Verzonden —	100,0 Mbps
Snelheid: Details Activiteit Bytes:	Verzonden — 1,131,653	Ontvangen

3.Double-click "Internet Protocol Version 4 (TCP/IPv4)" on the local connection properties page.



10000	
Verbinding maken via:	
🔮 Realtek PCIe G	BE Family Controller
	Configureren
Deze verbinding heeft	de volgende onderdelen nodig:
Client voor M	crosoft-netwerken
QoS-pakketn	lanner
- Coo paracorp	
Bestands- en	printerdeling voor Microsoft-netwerken
Bestands- en	printerdeling voor Microsoft-netwerken col versie 6 (TCP/IPv6)
Bestands- en Anternet Proto Anternet Proto Anternet Proto	printerdeling voor Microsoft-netwerken col versie 6 (TCP/IPv6) col versie 4 (TCP/IPv4)
Bestands- en internet Proto internet Proto internet Proto internet Proto internet Proto internet Proto	printerdeling voor Microsoft-netwerken col versie 6 (TCP/IPv6) col versie 4 (TCP/IPv4) ramma van Link-Layer Topology Discovery
Bestands- en Anternet Proto Anternet P	printerdeling voor Microsoft-netwerken col versie 6 (TCP/IPv6) col versie 4 (TCP/IPv4) amma van Link-Layer Topology Discovery pology Discovery Responder
Bestands- en Anternet Proto Anternet P	printerdeling voor Microsoft-netwerken col versie 6 (TCP/IPv6) col versie 4 (TCP/IPv4) ramma van Link-Layer Topology Discovery pology Discovery Responder
Bestands- en Antemet Proto	printerdeling voor Microsoft-netwerken col versie 6 (TCP/IPv6) col versie 4 (TCP/IPv4) ramma van Link-Layer Topology Discovery pology Discovery Responder
Bestands- en Ainternet Proto	printerdeling voor Microsoft-netwerken col versie 6 (TCP/IPv6) col versie 4 (TCP/IPv4) ramma van Link-Layer Topology Discovery pology Discovery Responder Verwijderen Eigenschappen
Bestands- en Antemet Proto Installeren Beschrijving	printerdeling voor Microsoft-netwerken col versie 6 (TCP/IPv6) col versie 4 (TCP/IPv4) ramma van Link-Layer Topology Discovery pology Discovery Responder Verwijderen Eigenschappen
Bestands- en Antemet Proto Antemet	printerdeling voor Microsoft-netwerken col versie 6 (TCP/IPv6) col versie 4 (TCP/IPv4) ramma van Link-Layer Topology Discovery pology Discovery Responder Verwijderen Verwijderen I Protocol/Internet Protocol. Het oor WAN-getwerken dat communicatie
Bestands- en Antemet Prote Antemet Antemet Prote Antemet Prote Antemet Prote Antemet An	printerdeling voor Microsoft-netwerken col versie 6 (TCP/IPv6) col versie 4 (TCP/IPv4) ramma van Link-Layer Topology Discovery pology Discovery Responder Verwijderen I Protocol/Internet Protocol. Het oor WAN-netwerken dat communicatie andersoortige, onderling met elkaar

- 4. There are two ways to configure the IP address of the PC
- Obtain IP address automatically (system default mode)
 To obtain an IP address automatically from a DHCP server, select "Obtain an IP address automatically";



Set a static IP address

Select "Use the following IP address" and set the correct values for the IP address, subnet mask and default gateway.



emeen P-instellingen kunnen automatisch worr eze mogelijkheid ondersteunt. Als dit n etwerkbeheerder naar de geschikte IP	den toegewezen als het netwerk niet het geval is, dient u de P-instellingen te vragen.
 Automatisch een IP-adres laten to Het volgende IP-adres gebruiken: 	bewijzen
IP-adres:	192 . 168 . 1 . 202
Subnetmasker:	255 .255 .255 . 0
Standaardgateway:	192.168.1.1
 Automatisch een DNS-serveradressen De volgende DNS-serveradressen 	s laten toewijzen gebruiken:
Voorkeurs-DNS-server:	192.168.1.1
Alternatieve DNS-server:	
🕅 Instellingen tijdens afsluiten valider	ren Geavanceerd

5.6.1.3 Configure Coupler IP address

There are 2 ways to assign an IP address

- Assignment via built-in web page (static IP or automatic IP assignment)
- Assign via DIP switch (static IP)

DIP address selector switch definition

Switch position (ON = 1)	Value	Definition
		Enable the DIP selector switch assignment
0000 0000		function and determine the value of the 3rd
1111 1110	0-254	byte.
		Example: 0010 0110 (22 decimal), the IP
		address is "192.168.22.253".
		Enable the function of specifying IP on the
1111 1111		web page, or select the function of DHCP
	255	automatic allocation. When the IP is not
		allocated through the web, the IP is
		192.168.1.10

5.6.1.3.1 Configuration via Web Page

The fieldbus coupler can be set to an IP address via the "Settings > Local Settings" page after entering the page, or it can be set to be assigned automatically. Select



static address, if not set IP address, the IP is 192.168.1.10



5.6.1.3.2 Assign IP via DIP Switch

Set the value of the DIP address selector switch to 0000 0000 - 1111 1110 (decimal 0 -254), and the IP address will be assigned by the DIP switch.

The IP address consists of fixed bytes and variable bytes. The 1st, 2nd and 4th bytes are fixed bytes, the DIP selector switch determines the 3rd byte, namely:

192.168.xxx.253

The fieldbus coupler assigns an IP address via a DIP switch, and the IP address set in this way is static.

	IP ADDR
8	00
	•
ON	Cv 🔲
	4
	– က
	Z
1	0

5.6.1.4 Factory Default Settings

Before logging into the web configuration page, it is necessary for you to understand the following default parameters,

IP: Determined according to the DIP switch, if the DIP switch is 1111 1111, the default IP is 192.168.1.10

If factory default DIP switch is 0000 0000 status, then the IP is 192.168.0.253

Item	Description	
30	Shenzhen Beilai Technology Co. Itd	V1 2



Username	admin
Password	Empty

5.6.2 Login Configuration Page

1. Open a browser on your computer, such as IE, Chrome, etc.

2.Enter the IP address of the coupler node (192.168.1.10) in the address bar of the browser to enter the user login interface.

0	新标	签页		×
\leftarrow	\rightarrow	C	0	192.168.1.10

3.Enter "Username" and "Password" in the login interface, and then click Login.

BL200UA					
Authorization Re	e quired default is admin) and pas	sword(no passv	vord by default).		
Username	admin				
Password					
					Login Reset

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4.After successfully logging in to the web interface, the display is as follows



BL200	Status -	System -	Settings -	I/O Module -	Serial Module 🕶	Operation Control -	Logout	REFRESHING
Status								
System								
Hostname				BL200				
Model				BL200-Modb	us TCP IO Module			
Firmware V	ersion			Shenzhen B	eilai Technology Co.	,Ltd. V1.1.12		
Kernel Vers	sion			4.4.194				
Local Time				2023-11-07 (08 <mark>:31:3</mark> 0			
Uptime				0h 6m 36s				
Load Avera	ge			1.39, 0.8 <mark>1</mark> , 0	.38			
Memory								
Total Availa	ble					26.77 MB / 56.5	9 MB (47%)	
Used						25.66 MB / 56.5	9 MB (45%)	
Buffered						3.34 MB / 56.5	9 MB (5%)	
Cached						9.50 MB / 56.59) MB (16%)	
Network								
Active Con	nections					74 / 16384	l (0%)	

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5.7 Web Configuration Page Description

5.7.1 Status

Users can check overview, system log and kernel log, as well as device parameters and device operating status.

Status > Overview



BL200UA	Status -	System -	Settings -	I/O Module -	Serial Module -	OPC UA -	Operation&Control -	Logout	REFRESHING
Status System	Overview System L Kernel L	log og							
Hostname			E	3L200UA					
Model			E	L200UA-OPCU	A IO Module				
Firmware Version	1		S	Shenzhe <mark>n</mark> Beilai	Technology Co.,Ltd	v1.0.11			
Kernel Version			4	.4.194					
Local Time			2	022-03-21 06:44	1:49				
Uptime			3	lh 31m 35s					
Load Average			C	0.16, 0.11 <mark>, 0.0</mark> 9					
Memory									
Total Available			Į			26.05 MB / 5	56.59 MB (46%)		
Used			1			26.57 MB / 5	56.59 MB (46%)		
Buffered)			3.21 MB / 5	56.59 MB (5%)		
Cached						9.98 MB / 5	6.59 MB (17%)		
Network									
Active Connection	ns					22 / 16	5384 (0%)		

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Status > System Log

By Settem Log Thu Jan 1 000.25 1970 kem info kernet [0.000000] Booling Linux on physical CPU 0x0 Thu Jan 1 000.25 1970 kem info kernet [0.000000] CPU: AMM2551.5 [141080258] revisions 5 (AMM5TEJ), cr=00053171 Thu Jan 1 000.25 1970 kem info kernet [0.000000] CPU: AMM2551.5 [141080258] revisions 5 (AMM5TEJ), cr=00053171 Thu Jan 1 000.25 1970 kem info kernet [0.000000] CPU: AMM2551.5 [141080258] revisions 5 (AMM5TEJ), cr=00053171 Thu Jan 1 000.25 1970 kem info kernet [0.000000] Memice model Nuvdro NLOSAND IOT-GateVay Version. 0 1 Thu Jan 1 000.25 1970 kem info kernet [0.000000] Nomel acros. 129 apps susked for memmap Thu Jan 1 000.25 1970 kem debug kernet [0.000000] Nomel acros. 129 apps susked for memmap Thu Jan 1 000.25 1970 kem debug kernet [0.000000] popu-alice: 010 data/page: 16326 Thu Jan 1 000.25 1970 kem debug kernet [0.000000] popu-alice: 010 data/page: 16326 Thu Jan 1 000.25 1970 kem into kernet [0.000000] popu-alice: 010 data/page: 16326 Thu Jan 1 000.25 1970 kem into kernet [0.000000] popu-alice: 100 data/page: 16326 Thu Jan 1 000.25 1970 kem into kernet [0.000000] popu-alice: 100 data/page: 13726 bres) Thu Jan 1 000.25 1970 kem into kernet [0.000000] popu-alice: 100 data/page: 13726 bres) Thu Jan 1 000.25 1970 kem into kernet [0.0000000]	BL200UA	Status -	System -	Settings -	I/O Module -	Serial Module +	OPC UA -	Operation&Control -	Logout
Au 10 00.025 1970 kem info kemit [0.00000] Booling Linux version 4.4.194 (peng@peng) (gc version 5.4.0 (LEDE GC 5.4.0 unknown)) #0 PREEMPT Sat May 9.15: Tu Jan 10 00.025 1970 kem info kemit [0.00000] CPU . ARRQES jervision 5 (ARRASTEJ), cr-00053177 Tu Jan 10 00.025 1970 kem info kemit [0.00000] Monty Delta (Statistical Statistical Stat	System L	.og							
The Jan 1 00002 61 1970 kem notice kernet: [0.00000] Lhux version 4.1 194 (peng@peng) (pcc version 5.4 0.LEDE GCC 5.4 0 unknown)) #0 PREEMPT Sat May 91 15: The Jan 1 00002 61 1970 kem info kernet: [0.00000] CPL: VNVT data cache, VIVT instruction cache Thu Jan 1 00002 61 1970 kem info kernet: [0.00000] Machine model: Nuvdon NLOS90 10T-GateVky Version: 0.1 Thu Jan 1 00002 61 1970 kem info kernet: [0.00000] Machine model: Nuvdon NLOS90 10T-GateVky Version: 0.1 Thu Jan 1 00002 61 1970 kem debug kernet: [0.00000] Infe_are=_init_node: node 0.pdd1 c057704, node_mem_map c3f77000 Thu Jan 1 00002 61 1970 kem debug kernet: [0.00000] Normal zone: 120 pages used for memmap Thu Jan 1 00002 61 1970 kem debug kernet: [0.00000] Normal zone: 120 pages used for memap Thu Jan 1 00002 61 1970 kem debug kernet: [0.000000] Normal zone: 130 pages, LIFO batch: 3 Thu Jan 1 00002 61 1970 kem debug kernet: [0.000000] pcpu-alloc: 80 f 032768 u32768 alloc-1*32768 Thu Jan 1 00002 61 1970 kem debug kernet: [0.000000] pcpu-alloc: 80 f 032768 u32768 alloc-1*32768 Thu Jan 1 00002 61 1970 kem into kernet: [0.000000] pcpu-alloc: 80 f 032768 u32768 alloc-1*32768 Thu Jan 1 00002 61 1970 kem into kernet: [0.000000] pcpu-alloc: 80 f 032768 u32768 alloc-1*32768 Thu Jan 1 00002 61 1970 kem into kernet: [0.000000] pcpu-alloc: 80 f 032768 u32768 alloc-1*32768 Thu Jan 1 00002 61 1970 kem into kernet: [0.000000] pcpu-alloc: 80 f 032768 u32768 alloc-1*32768 Thu Jan 1 00002 61 1970 kem into kernet: [0.000000] pcpu-alloc: 80 f 032768 u32768 alloc-1*32768 Thu Jan 1 00002 61 1970 kem into kernet: [0.000000] pcpu-alloc: 80 f 032768 u32768 alloc-1*32768 Thu Jan 1 00002 61 1970 kem into kernet: [0.000000] prevalue: 40 kem 1000 (3072 kem) Thu Jan 1 00002 61 1970 kem into kernet: [0.000000] prevalue: 40 kem 1000 (3072 kem) Thu Jan 1 00002 61 1970 kem into kernet: [0.000000] prevalue: 0.0000000 (3072 kem) Thu Jan 1 00002 61 1970 kem into kernet: [0.000000] fermer 3756kr55354 m kem et dea, 3050 km km kem 10000000 (3072 kem) Thu	Thu Jan 1 00:00:2	6 1970 kem	info kernel: [0.000000] Bo	oting Linux on ph	ysical CPU 0x0			
The Jan 1 00002 to 1970 kem info kernet [0 000000 (CPU VMV faits cache VVV instruction cache (ARM-STEJ), c=0005317f (Mait Line Merrier) (0 000000 (CPU VVV faits cache, VVV instruction cache (Mait Line Merrier) (0 000000 (CPU VVV instruction cache (Mait Line Merrier) (0 000000 (CPU VVV instruction cache (Mait Line Merrier) (0 000000 (CPU VVV instruction cache (Mait Line Merrier) (0 000000 (CPU VVV instruction cache (Mait Line Merrier) (0 000000 (CPU VVV instruction cache (Mait Line Merrier) (Mait Line Merrier) (Mait Line (Thu Jan 1 00:00:2	6 1970 kem	notice kernel:	[0.000000]	Linux version 4.4	194 (peng@peng) (go	c version 5.4.0 (LEDE GCC 5.4.0 unknow	n))#0 PREEMPT Sat May 9 15:2
Thu Jan 10 00:0028 1970 kem infs kernel [00:0000000000000000000000000000000000	hu Jan 1 00:00:2	6 1970 kem	info kernel: [0.0000001 CF	U: ARM926EJ-S	[41069265] revision 5	(ARMv5TEJ), c	r=0005317f	
The Jan 1 00:0022 1970 kem info kernet [00:00000 00:00000 100:0022 1970 kem info kernet [00:000000 00:00000 100:0022 1970 kem info kernet [0:000000 10:000000 10:000000 10:000000 10:000000 10:000000 10:000000 10:000000 10:000000 10:000000 10:000000 10:000000 10:000000 10:000000 10:0000000 10:0000000 10:0000000 10:00000000 10:000000000000 10:00000000000000000000000000000000000	hu Jan 1 00:00:2	6 1970 kem	info kernel: [0.0000001 CF	U: VIVT data cad	he. VIVT instruction c	ache		
The Jan 1000028 1970 kem kinfo kernel [0.000000] Memory policy: Data cache writeback: Thu Jan 1000028 1970 kem debug kernel [0.000000] Nomal zone: 1084 pages used for memmap Thu Jan 1000028 1970 kem debug kernel [0.000000] Nomal zone: 20 pages used for memmap Thu Jan 1000028 1970 kem debug kernel [0.000000] Nomal zone: 109 pages used for memmap Thu Jan 1000028 1970 kem debug kernel [0.000000] pepu-alloc: 91 d 32768 J32768 J3000-1132768 Thu Jan 1000028 1970 kem debug kernel [0.000000] pepu-alloc: 91 d 32768 J32768 J3000-1132768 Thu Jan 1000028 1970 kern debug kernel [0.000000] pepu-alloc: 91 d 32768 J32768 J3000-1132768 Thu Jan 1000028 1970 kern debug kernel [0.000000] pepu-alloc: 91 d 32768 J32768 J3000-1132768 Thu Jan 1000028 1970 kern info kernel [0.000000] pepu-alloc: 91 d 32768 J32768 J3000-1132768 Thu Jan 1000028 1970 kern info kernel [0.000000] pepu-alloc: 91 d 32768 J32768 J3000-1132768 Thu Jan 1000028 1970 kern info kernel [0.000000] pepu-alloc: 91 d 4256 (decr. 3. 132768 Jb1290) Thu Jan 1000028 1970 kern info kernel [0.000000] Jonde- cache hash table entries: 4096 (order: 3. 32768 Jb129) Thu Jan 1000028 1970 kern info kernel [0.000000] forma: 0.offici0000 - 0.offi00000 (46538 Jb128) Thu Jan 1000028 1970 kern info kernel [0.000000] forma: 0.offici0000 - 0.offi00000 (448) Thu Jan 1000028 1970 kern notice kernel [0.000000] forma: 0.offici0000 - 0.offi00000 (448) Thu Jan 1000028 1970 kern notice kernel [0.000000] forma: 0.ocfici00000 - 0.offi00000 (448) Thu Jan 1000028 1970 kern notice kernel [0.000000] forma: 0.ocfici00000 - 0.ocfi000000 (448) Thu Jan 1000028 1970 kern notice kernel [0.000000] forma: 0.ocfici00000 - 0.ocfi000000 (448) Thu Jan 1000028 1970 kern notice kernel [0.000000] forma: 0.ocfici00000 - 0.ocfi000000 (448) Thu Jan 1000028 1970 kern notice kernel [0.000000] forma: 0.ocfici00000 - 0.ocfi000000 (448) Thu Jan 1000028 1970 kern notice kernel [0.000000] forma: 0.ocfici00000 (448) Thu Jan 1000028 1970 kern intice kernel [0.000000] forma: 0.ocfici0000	hu Jan 1 00:00:2	6 1970 kem	info kernel: [0.0000001 Ma	achine model: Nur	voton NUC980 IOT-Ga	teWay Version:	0.1	
hu Jan 100002 26 1970 kem debug kemei [0.000001 On node 0 totalpages: 16384 hu Jan 100002 26 1970 kem debug kemei [0.000001 Normal zone: 128 pages used for memmap hu Jan 100002 26 1970 kem debug kemei [0.000001 Normal zone: 128 pages used for memmap hu Jan 100002 26 1970 kem debug kemei [0.000000 Normal zone: 16384 pages. LIPO batch 3 hu Jan 100002 26 1970 kem debug kemei [0.000000 prot-alloc: 901 0 32768 balloc-1*32768 hu Jan 100002 26 1970 kem debug kemei [0.000000 prot-alloc: 901 0 32768 balloc-1*32768 hu Jan 100002 26 1970 kem inforkermei [0.000000 prot-alloc: 901 0 32755 G(order: 2, 1024 bytes) hu Jan 100002 26 1970 kem inforkermei [0.000000 prot-ache hash table entries: 555 (order: 2, 16384 bytes) hu Jan 100002 26 1970 kem inforkermei [0.00000 prot-ache hash table entries: 555 (order: 2, 16384 bytes) hu Jan 100002 26 1970 kem inforkermei [0.00000 prot-ache hash table entries: 408 (order: 2, 16384 bytes) hu Jan 100002 26 1970 kem inforkermei [0.00000 prot-oche intries: 408 (order: 2, 16384 bytes) hu Jan 100002 26 1970 kem inforkermei [0.00000 prot-oche isyst hu Jan 100002 26 1970 kem inforkermei [0.00000 prot-oche isyst hu Jan 100002 26 1970 kem inforkermei [0.00000 prot-oche isyst hu Jan 100002 26 1970 kem inforkermei [0.00	hu Jan 1 00:00:2	6 1970 kem	info kernel: [0.0000001 Me	mory policy: Data	a cache writeback			
The Uan 1 00:02:36 1970 kern debug kernet [0 000000] free_ares_init_node_nem_fore_mem_ap c3777000 Thu Jan 1 00:02:36 1970 kern debug kernet [0 000000] Normal zone: 29 apges used for memmap Thu Jan 1 00:02:36 1970 kern debug kernet [0 000000] Pormal zone: 29 apges used for memmap 10:00:26 1970 kern debug kernet [0 000000] Porpu-altoc: 80 f0 d32768 u32788 altoc=1'32768 Thu Jan 1 00:02:36 1970 kern debug kernet [0 000000] Porpu-altoc: 80 f0 d32768 u32788 altoc=1'32768 Thu Jan 1 00:02:36 1970 kern info kernet [0 000000] Porpu-altoc: 80 f0 d32768 u32788 altoc=1'32768 Thu Jan 1 00:02:36 1970 kern info kernet [0 000000] Porpu-altoc: 80 f0 d32768 u32788 altoc=1'32768 Thu Jan 1 00:02:36 1970 kern info kernet [0 000000] PlD hash table entries: roto-ide/mthuBlock2 console=ttyS0, 115200n8 rdinit=/abininit mem=64M lpi=744448 Thu Jan 1 00:02:36 1970 kern info kernet [0 000000] PlD hash table entries: roto-ide/mthuBlock2 console=ttyS0, 115200n8 rdinit=/abininit mem=64M lpi=744448 Thu Jan 1 00:02:36 1970 kern info kernet [0 000000] Port zoche hash table entries: roto-ide/mthuBlock2 console=ttyS0, 115200n8 rdinit=/abininit mem=64M lpi=744448 Thu Jan 1 00:02:36 1970 kern info kernet [0 000000] Port zoche hash table entries: roto-ide/mthuBlock2 console=ttyS0, 115200n8 rdinit=/abininit mem=64M lpi=74448 Thu Jan 1 00:02:36 1970 kern info kernet [0 000000] Virtual kernet Internory layout: Thu Jan 1 00:00:26 1970 kern info kernet [0 000000] Portor: rotoff00000 (3072 kB) Thu Jan 1 00:00:26 1970 kern infolk kernet [0 000000] return is 0xcf000000 (3072 kB) Thu Jan 1 00:00:26 1970 kern infolk kernet [0 000000] return is 0xcf000000 (44 kB) Thu Jan 1 00:00:26 1970 kern infolk kernet [0 000000] return is 0xcf000000 (15 MB) Thu Jan 1 00:00:26 1970 kern infolk kernet [0 000000] return is 0xcf000000 (16 kB) Thu Jan 1 00:00:26 1970 kern infolk kernet [0 000000] return is 0xcf000000 (16 kB) Thu Jan 1 00:00:26 1970 kern infolk kernet [0 000000] return is 0xcf000000 (16 kB) Thu Jan 1 00:00:26 1970 kern infolk kernet [0 000000]	hu Jan 1 00:00:2	6 1970 kem	debug kemel;	100000000	On node 0 totalp	ages: 16384			
The Jan 1 00:02:36 1970 kern debug kernet [0 000000] Normal zone: 128 pages used for memmap the second sec	hu Jan 1 00:00:2	6 1970 kem	debug kernel:	[0.000000]	free area init no	de: node 0, pgdat c06	57704, node m	em map c3f77000	
The Jan 1 00:02:6 1970 kern debug kernet [0 000000] Normal zone: 0 page reserved [0.00000] Popu-alloc: 30 r0 d32768 u32768 alloc=1*32768 [0.00000] Popu-alloc: 20 r0 dots [0.00000] Popu-alloc: 25 (roder: 2, 1024 bytes) [0.00000] Popu-alloc:	Thu Jan 1 00:00:2	6 1970 kem	debug kemel;	100000001	Normal zone: 12	28 pages used for mer	nmap	-	
Thu Jan 10:00:26 1970 kem debug kemel; 0.000000 popu-alloc: 20 01 032768 u32768 alloc=1132768 Thu Jan 10:00:26 1970 kem debug kemel; 0.000000 popu-alloc: 20 01 032768 u32768 alloc=1132768 Thu Jan 10:00:26 1970 kem debug kemel; 0.000000 popu-alloc: 20 01 032768 u32768 alloc=1132768 Thu Jan 10:00:26 1970 kem notice kemel; 0.000000 popu-alloc: 20 1070 kem info kemel; 0.000000 popu-alloc: 20 1070 kem info kemel; Thu Jan 10:00:26 1970 kem info kemel; 0.000000 popu-alloc: 20 1070 kem info kemel; 0.000000 popu-alloc: 20 1070 kem info kemel; Thu Jan 10:00:26 1970 kem info kemel; 0.000000 popuro;57756/5565364 avalatel (4535K kemel code, 305K rwdata, 1704K rodata, 183K init, 252K bss, 7780K res Thu Jan 10:00:26 1970 kem notice kemel; 0.000000 predict (4535K kemel code, 305K rwdata, 1704K rodata, 183K init, 252K bss, 7780K res Thu Jan 10:00:26 1970 kem notice kemel; 0.000000 predict (4535K kemel code, 305K rwdata, 1704K rodata, 183K init, 252K bss, 7780K res Thu Jan 10:00:26 1970 kem notice kemel; 0.000000 predict (45800000 - 0xf800000 (3474 kg) Thu Jan 10:00:26 1970 kem notice kemel; 0.000000 predict (4500000 - 0xf800000 (448) Thu Jan 10:00:26 1970 kem notice kemel; 0.000000 predict (45000000 (4480) Thu Jan <td>Thu Jan 1 00:00:2</td> <td>6 1970 kem</td> <td>debug kernel:</td> <td>[0.000000]</td> <td>Normal zone: 0</td> <td>pages reserved</td> <td></td> <td></td> <td></td>	Thu Jan 1 00:00:2	6 1970 kem	debug kernel:	[0.000000]	Normal zone: 0	pages reserved			
The Jan 1 0002 of 1970 kem debug femel: [0.00000] popu-alloc: 90 / 0 432768 ditc=1*32768 ditt=1*32768 ditt=1*32768 ditc=1*32768 ditt=1	Thu Jan 1 00:00:2	6 1970 kem	debug kemel;	100000001	Normal zone: 16	384 pages, LIFO bato	:h:3		
Thu Jan 10:00:26 1970 kem debug termel: 0:000000 pcpu-alloc: [0] 0 Thu Jan 10:00:26 1970 kem notice kernel: 0:000000 jcmit 1 zoneitals in zone order, mobility grouping on. Total pages: 16:256 Thu Jan 10:00:26 1970 kem notice kernel: 0:000000 jcmit 1 zoneitals in zone order, mobility grouping on. Total pages: 16:256 Thu Jan 10:00:26 1970 kem info kernel: 0:000000 jcmit zoneitals: 256 (order: 2, 16:384 bytes) Thu Jan 10:00:26 1970 kem info kernel: 0:000000 jcmit zoneitals in kernel: 0:000000 jcmit zoneitals in zoneital	Thu Jan 1 00:00:2	6 1970 kem	debug kernel:	[0.000000]	pcpu-alloc: s0 r0	d32768 u32768 alloc=	1*32768		
Thu Jan 100026 1970 kem info kernel; 0.000000 Built 1 zonelsta in Zone order, mobility grouping on. Total pages: 16256 Thu Jan 100026 1970 kem info kernel; 0.000000 PID bash table entries: 155 (order: 3, 12768 bytes) Thu Jan 100026 1970 kem info kernel; 0.000000 PID bash table entries: 155 (order: 3, 12768 bytes) Thu Jan 100026 1970 kem info kernel; 0.000000 Memory: 57756K/55536K available (4350K kernel code, 305K rwdata, 1704K rodata, 188K init, 252K bss, 7780K res Thu Jan 100026 1970 kem notice kernel; 0.000000 Memory: 57756K/55536K available (4350K kernel code, 305K rwdata, 1704K rodata, 188K init, 252K bss, 7780K res Thu Jan 100026 1970 kem notice kernel; 0.000000 Memory: 57756K/55536K available (4350K kernel code, 305K rwdata, 1704K rodata, 188K init, 252K bss, 7780K res Thu Jan 100026 1970 kem notice kernel; 0.000000 Memory: 57756K/5536K available (4350K kernel code, 305K rwdata, 1704K rodata, 188K init, 252K bss, 7780K res Thu Jan 100026 1970 kem notice kernel; 0.000000 Memory: 50756K/5536K available (4350K kernel code) Thu Jan 100026 1970 kem notice kernel; 0.000000 Memory: 50756K/5050K available (4350K kernel code) Thu Jan 100026 1970 kem notice kernel; 0.000000 Memory: 50756K/5000000 (64 MB) Thu Jan 100026 1970 kem notice kernel; 0.000000 Memory: 50756K/5000000 (64 MB) Thu Jan 100026 1970 kem notice kernel; 0.000000 Memory: 5075656 Karoko000000 (64 MB) Th	Thu Jan 1 00:00:2	6 1970 kem	debug kernel:	[0.000000]	pcpu-alloc: [0] 0				
Thu Jan 10:00:26 1970 kem notice kernel: 0:00:00000000000000000000000000000000	Thu Jan 1 00:00:2	6 1970 kem	info kernel: [0.0000001 Bu	ilt 1 zonelists in Z	one order, mobility gro	uping on. Total	pages: 16256	
The Jan 1 00026 1970 kem info kernel [0 000000] PiD hash table entries: 256 (order: 2, 1024 ptes) [hu Jan 1 00026 1970 kem info kernel [0 000000] Inde-cache hash table entries: 4096 (order: 2, 16384 bytes) hu Jan 1 00026 1970 kem info kernel [0 000000] Memory: 57756/K55368 vaniable (4330K kernel code, 305K rvdata, 1704K rodata, 188K init, 252K bss, 7780K res hu Jan 1 000226 1970 kem notice kernel [0 000000] Virtual kernel memory layout hu Jan 1 000226 1970 kem notice kernel [0 000000] Virtual kernel memory layout hu Jan 1 000226 1970 kem notice kernel [0 000000] vector : 0dfff0000 - 0dffB00000 (3072 kB) hu Jan 1 000226 1970 kem notice kernel [0 000000] vector : 0dfff0000 - 0dfB00000 (3072 kB) hu Jan 1 000226 1970 kem notice kernel [0 000000] vector : 0dfff0000 - 0dfB00000 (3072 kB) hu Jan 1 000226 1970 kem notice kernel [0 000000] vector : 0dfff0000 - 0dfB000000 (3072 kB) hu Jan 1 000226 1970 kem notice kernel [0 000000] vector : 0dfff0000 - 0dfB000000 (3072 kB) hu Jan 1 000226 1970 kem notice kernel [0 000000] vector : 0ddfB0000 - 0dfB000000 (3072 kB) hu Jan 1 000226 1970 kem notice kernel [0 000000] vector : 0ddfB0000 - 0dfB000000 (3072 kB) hu Jan 1 000226 1970 kem notice kernel [0 000000] vector : 0ddfB000 - 0ddfB00000 (3072 kB) hu Jan 1 000226 1970 kem notice kernel [0 000000] vector : 0ddfB000 - 0ddfB00000 0 0dfB00000 (3072 kB) hu Jan 1 000226 1970 kem notice kernel [0 000000] vector : 0ddfB00 - 0ddfB0000 - 0ddfB00000 - 0ddfB0000 - 0ddfB0000 - 0ddfB0000 - 0ddfB0000 - 0ddfB0000 - 0ddfB0000 - 0ddfB00000 - 0ddfB0000 - 0ddfB00000 - 0ddfB00000 - 0ddfB0000 - 0ddfB00000 - 0ddfB0000 - 0ddfB00000 - 0ddfB0000 - 0ddfB0000 - 0ddfB00000	Thu Jan 1 00:00:2	6 1970 kem	notice kernel:	100000000	Kernel command	line: root=/dev/mtdblo	ck2 console=tty	S0.115200n8 rdinit=/sbin/i	nit mem=64M lpi=744448
Thu Jan 100026 1970 kem info kemel; 0.000000 Dentry cache hash table entries: 8052 (order: 2, 1538 bytes) Thu Jan 100026 1970 kem info kemel; 0.00000 Dentry cache hash table entries: 8052 (order: 2, 1538 bytes) Thu Jan 100026 1970 kem info kemel; 0.00000 Dentry 7575K/6535K available (4538 kemel cace) 305K rvidata, 1704K rodata, 188K init, 252K bes, 7780K res Thu Jan 100026 1970 kem indice kemel; 0.00000 Dentry 7575K/6535K available (4538 kemel cace) 305K rvidata, 1704K rodata, 188K init, 252K bes, 7780K res Thu Jan 100026 1970 kem indice kemel; 0.00000 Virual kernel memory layout Thu Jan 1000226 1970 kem indice kemel; 0.00000 Virual kernel memory layout Thu Jan 1000226 1970 kem indice kemel; 0.00000 Virual kernel memory layout Thu Jan 1000226 1970 kem indice kemel; 0.00000 Virual kernel memory layout Thu Jan 1000226 1970 kem indice kemel; 0.00000 Virual kernel kernel kernel 0.000000 Virual kernel kernel kernel Thu Jan 1000226 1970 kern indice kemel; 0.000000 Virual kernel kernel kernel 0.000000 Virual kernel kernel 0.000000 Virual kernel kernel Thu Jan 1000226 1970 kern indice kernel; 0.000000 Virual kernel kernel 0.000000 Virual kernel kernel 0.000000 Virual kernel kernel Thu Jan 1000226 1970 kern indice kernel; 0.000000 Virual kernel kernel 0.0000000 Virual kernel kernel 0.0000000	Thu Jan 1 00:00:2	6 1970 kem	info kernel: [0.0000001 PI	D hash table entri	es: 256 (order: -2, 102	4 bytes)		
The Jan 1 00:02:6 1970 kem info kernel; 0 000000 [Inode-cache hash table entries: 4096 [order: 2, 16384 bytes] Thu Jan 1 00:02:6 1970 kem notice kernel; 0 000000 [Inode-cache hash table entries: 4096 [order: 2, 16384 bytes] Thu Jan 1 00:02:6 1970 kem notice kernel; 0 000000 [Inode-cache hash table entries: 4086 [Inode: 2, 16384 bytes] Thu Jan 1 00:02:6 1970 kem notice kernel; 0 000000 [Inode-Cache hash table entries: 4086 [Inode: 2, 16384 bytes] Thu Jan 1 00:02:6 1970 kem notice kernel; 0 000000 [Inode-Cache Mathematy Japant Thu Jan 1 00:02:6 1970 kem notice kernel; 0 000000 [Inode: 0xdf00000 - 0xdf00000 (S072 kB) Thu Jan 1 00:02:6 1970 kem notice kernel; 0 000000 [Inode: 0xdf00000 - 0xdf000000 (Inder MB) Thu Jan 1 00:02:6 1970 kem notice kernel; 0 000000 [Inode: 0xdf000000 - 0xdf000000 (Inder MB) Thu Jan 1 00:02:6 1970 kem notice kernel; 0 000000 [Inode: 0xdf00000 0 xcd000000 (Inder MB) Thu Jan 1 00:02:6 1970 kem notice kernel; 0 000000 [Inder X: 0xc000000 - 0xc0000000 (Inder MB) Thu Jan 1 00:02:6 1970 kem notice kernel; 0 000000 [Inder X: 0xc000000 0 xcd000000 (Inder MB) Thu Jan 1 00:02:6 1970 kem notice kernel; 0 000000 [Inder X: 0xc000000 0 xcd000000 (Inder MB) Thu Jan 1 00:02:6 1970 kem notice kernel; 0 000000 [Inder X: 0xc000000 0 xcd00000 (Inder MB) Thu Jan 1 00:02:6 1970 kem notice kernel; 0 000000 [Inder MB) Thu Jan 1 00:02:6 1970 kem info kernel; 0 000000 [Inder MB] Thu Jan 1 00:02:6 1970 kem info kernel; 0 000000 [Inder MB] Thu Jan 1 00:02:6 1970 kern info kernel; 0 000000 [Inder MB] Thu Jan 1 00:02:6 1970 kern info kernel; 0 000000 [Inder MB] Thu Jan 1 00:02:6 1970 kern info kernel; 0 000000 [Inder MB] Thu Jan 1 00:02:6 1970 kern info kernel; 0 000000 [Inder MB] Thu Jan 1 00:02:6 1970 kern info kernel; 0 000000 [Inder MB] Thu Jan 1 00:02:6 1970 kern info kernel; 0 000000 [Inder MB] Thu Jan 1 00:02:6 1970 kern info kernel; 0 000000 [Inder MB] Thu Jan 1 00:02:6 1970 kern info kernel; 0 0:00000 [Inder MB] Thu Jan 1 0:00:26 1970 kern info kernel; 0 0:00000 [Inder MB] Thu Jan 1 0	Thu Jan 1 00:00:2	6 1970 kem	info kernel: [0.0000001 De	entry cache hash	table entries: 8192 (or	der: 3, 32768 by	tes)	
Thu Jan 100026 1970 kem info kernel; 0.000000 [Memory: 5775K/K535K available (4538K kernel code; 305K rwdata, 1704K rodata, 188K init, 252K bss, 7780K res Thu Jan 100026 1970 kem notice kernel; 0.000000 [wrdato: 0.00000 [Memory layout] Thu Jan 100026 1970 kem notice kernel; 0.000000 [msmap: 0.0fft0000 - 0.offt00000 (344 MB) Thu Jan 100026 1970 kem notice kernel; 0.000000 [msmap: 0.offt00000 - 0.offt00000 (344 MB) Thu Jan 100026 1970 kem notice kernel; 0.000000 [msmap: 0.offt00000 - 0.offt00000 (344 MB) Thu Jan 100026 1970 kem notice kernel; 0.000000 [msmap: 0.offt00000 - 0.ocf000000 (164 MB) Thu Jan 100026 1970 kem notice kernel; 0.000000 [msmap: 0.offt00000 - 0.ocf000000 (168 MB) Thu Jan 100026 1970 kem notice kernel; 0.000000 [msmap: 0.offt0000 - 0.ocf050000 (186 KB) Thu Jan 100026 1970 kem notice kernel; 0.000000 [msmap: 0.offt000 - 0.ocf050000 (186 KB) Thu Jan 100026 1970 kem notice kernel; 0.000000 [msmap: 0.offt000 - 0.ocf050000 (186 KB) Thu Jan 100026 1970 kem notice kernel; 0.000000 [msmap: 0.offt000 - 0.ocf050000 (186 KB) Thu Jan 100026 1970 kem info kernel; 0.000000 [msmap: 0.offt000 - 0.ocf050000 (186 KB) Thu Jan 100026 1970 kem info kernel; 0.000000 [msmap: 0.offt000 - 0.ocf050000 (186 KB) Thu Jan 100026 1970 kem info kernel; 0.0000000 [msmap: 0.offt000 - 0.ocf05000 (0.0CF44 (0.06 KB)	Thu Jan 1 00:00:2	6 1970 kem	info kernel: [0.0000001 Inc	de-cache hash ta	able entries: 4096 (ord	er: 2, 16384 byte	es)	
Thu Jan 1 00:02:6:1970 kem notice keme! 0.0000001 Virtual keme mony layout: Thu Jan 1 00:02:6:1970 kem notice keme! 0.0000001 vector: 0.0rtf000000 -0.0rtf100000 (0:072 kB) Thu Jan 1 00:02:6:1970 kem notice keme! 0.0000001 wmail:: 0xx4800000 - 0.ortf000000 (0:4 kB) Thu Jan 1 00:02:6:1970 kem notice keme! 0.0000001 wmail:: 0xx4800000 - 0.ortf0000000 (0:4 kB) Thu Jan 1 00:02:6:1970 kem notice keme! 0.0000001 wmail:: 0.xx06500000 (0:64 MB) Thu Jan 1 00:02:6:1970 kem notice keme! 0.0000001 wmail:: 0.xx0650000 0.xx06050000 (164 MB) Thu Jan 1 00:02:6:1970 kem notice keme! 0.0000001 wmail:: 0.xx0650000 0.xx0650000 (168 kB) Thu Jan 1 00:02:6:1970 kem notice keme! 0.0000001 wmail:: 0.xx0650000 0.xx0650000 (168 kB) Thu Jan 1 00:02:6:1970 kem notice keme! 0.0000001 below:: 0.xx0650000 0.xx0650000 0.xx0650000 Thu Jan 1 00:02:6:1970 kem info keme! 0.0000001 Preemptible Hivarchical RCU implementation. Thu Jan 1 0:00:2:6:1970 kem info keme! 0.0000001 NE_R2S:545 Thu Jan 1 0:00:2:6:1970 kem info keme! 0:000031 Siche:: coluri dimsi dis33:s, wraps every 63905052459 s Thu Jan 1 0:00:2:6:1970 kem info keme! 0:000031 Siche:: colurd dimsi dis33:s, wraps e	Thu Jan 1 00:00:2	6 1970 kem	info kernel: [0.0000001 Me	emory: 57756K/65	536K available (4538	K kernel code, 3	05K rwdata, 1704K rodata	a. 188K init. 252K bss. 7780K res
Thu Jan 1 00.02.6 1970 kem notice kemel; 0.000000 vector: 0xm0000_0xm10000_0xm10000_0xm10000_0xm10000_0xm10000_0xm10000_0xm10000_0xm10000_0xm10000_0xm10000_0xm10000_0xm10000_0xm10000_0xm10000_0xm10000_0xm10000_0xm100000_0xm10000_0xm100000_0xm100000_0xm100000_0xm100000_0xm100000_0xm100000_0xm100000_0xm100000_0xm100000_0xm100000_0xm100000_0xm100000_0xm10000_0xm10000_0xm10000_0xm10000_0xm10000_0xm100000_0xm100000_0xm100000_0xm100000_0xm100000_0xm100000_0xm100000_0xm10000_0xm10000_0xm10000_0xm10000_0xm100000_0xm10000_0xm10000_0xm10000_0xm100000_0xm100000_0xm100000_0xm100000_0xm100000_0xm100000_0xm10000_0xm100000_0xm100000_0xm100000_0xm100000_0xm100000_0xm100000_0xm100000_0xm100000_0xm100000_0xm100000_0xm100000_0xm100000_0xm100000_0xm100000_0xm1000000_0xm1000000_0xm100000_0xm1000000_0xm1000000_0xm10000000_0xm10000000_0xm10000000_0xm10000000_0xm1000000_0xm1000000_0x	hu Jan 1 00:00:2	6 1970 kem	notice kernel:	1000000.0	Virtual kernel mer	mory layout:			
Thu Jan 1 100 00.26 1970 kem notice kemel; 0.000000; formalic: 0xx4800000 - 0xtf000000; 044 MB) Thu Jan 1 0x0 02.6 1970 kem notice kemel; 0.000000; lowx4800000; 0x4800000; 044 MB) Thu Jan 1 0x0 02.6 1970 kem notice kemel; 0.000000; lowx4800000; 0x4800000; 0x4800000; 0x4800000; 0x4800000; 0x4800000; 0x4800000; 0x4800000; 0x4800000; 0x6400000; 0x4800000; 0x6400000; 0x64000; 0x64000; 0x64000; 0x64000; 0x6400; 0x6400; 0x6400; 0x6400; 0x64	Thu Jan 1 00:00:2	6 1970 kem	notice kernel:	10000000	vector : 0xffff0	000 - 0xffff1000 (4)	(B)		
Thu Jan 1 00.02.6 1970 kem notice kemel; 0.000000] vmmail c: 0xc4000000 0xd8000000 644 MB) Thu Jan 1 00.02.6 1970 kem notice kemel; 0.000000] inverse 0xc0000000 644 MB) Thu Jan 1 00.02.6 1970 kem notice kemel; 0.000000] inverse 0xc0008000 - 0xc0000000 644 MB) Thu Jan 1 0x0.02.6 1970 kem notice kemel; 0.000000] int: 0xc0621004 - 0xc0620054 (6244 kB) Thu Jan 1 0x0.02.6 1970 kem notice kemel; 0.000000] int: 0xc0621004 - 0xc0620054 (6244 kB) Thu Jan 1 0x0.02.6 1970 kem notice kemel; 0.000000] ist: 0xc0652006 - 0xc0650064 (63 kB) Thu Jan 1 0x0.02.6 1970 kem indice kemel; 0.000000] ist: 0xc0652006 - 0xc065006724 (0306 kB) Thu Jan 1 0x0.02.6 1970 kem indo kemel; 0.000000] SUIE HVMign=32, Order=0-3, MinObject=0, CPU=1, Nodes=1 Thu Jan 1 0x0.02.6 1970 kem indo kemel; 0.000000] SUIE-HVMign=32, Order=0-3, MinObject=0, CPU=1, Nodes=1 Thu Jan 1 0x0.02.6 1970 kem indo kemel; 0.000000] SUIE-HVMign=32, Order=0-3, MinObject=0, CPU=1, Nodes=1 Thu Jan 1 0x0.02.6 1970 kem indo kemel; 0.000000] SUIE-HVMign	Thu Jan 1 00:00:2	6 1970 kem	notice kernel:	1000000.0	fixmap : 0xffc0	0000 - 0xfff00000 (3)	072 kB)		
Thu Jan 1 00 00 26 1970 kem notice kemel; 0 000000 lowmem 1 0x000000 - 0xc4000000 (64 MB) Thu Jan 1 00 00 26 1970 kem notice kemel; 0 000000 tx1 0xc000000 - 0xc4000000 (16 MB) Thu Jan 1 00 00 26 1970 kem notice kemel; 0 000000 tx1 0xc000000 - 0xc0000000 (16 MB) Thu Jan 1 00 00 26 1970 kem notice kemel; 0 000000 tx1 0xc000000 - 0xc0000000 (16 MB) Thu Jan 1 00 00 26 1970 kem notice kemel; 0 000000 tx1 0xc00000 - 0xc0000000 (16 KB) Thu Jan 1 00 00 26 1970 kem notice kemel; 0 000000 tx1 0xc00000 - 0xc0000000 - 0xc0000000 (16 KB) Thu Jan 1 00 00 26 1970 kem notice kemel; 0 0000000 tx1 0xc00000 - 0xc000000 - 0xc0000000 tx1 0xc00000 Thu Jan 1 00 00 26 1970 kem info kemel; 0 0000000 tx1 0xc00000 - 0xc000000 tx1 0xc00000 tx1 0xc00000 Thu Jan 1 00 00 26 1970 kem info kemel; 0 0000000 10 kL1 HV/310mA23, Crder-0-3, MIN Object=0, CPU=1, Nodes=1 tx1 0xc0000000000000000000000000000000000	Thu Jan 1 00:00:2	6 1970 kem	notice kernel:	10000000	vmalloc : 0xc48	300000 - 0xff800000	(944 MB)		
Thu Jan 1 00:02:61970 kem notice kemel: 0.0000001 modules: 0:x0000000 (16 MB) Thu Jan 10:00:261970 kem notice kemel: 0.0000001 ind: 0:x0050000 (16 MB) Thu Jan 10:00:261970 kem notice kemel: 0.0000001 ind: 0:x0050000 (188 kB) Thu Jan 10:00:261970 kem notice kemel: 0.0000001 ind: 0:x0050000 (188 kB) Thu Jan 10:00:261970 kem notice kemel: 0.0000001 isx0050000 (253 kB) Thu Jan 10:00:261970 kem info kemel: 0.0000001 Suc050000 (253 kB) Thu Jan 10:00:261970 kem info kemel: 0.0000001 Suc050000 CUB CUB Thu Jan 10:00:261970 kem info kemel: 0.0000001 Build-time adjustment of leaf fanout to 32. Nu Jan 10:00:261970 kem info kemel: 0.0000001 Thu Jan 10:00:261970 kem info kemel: 0.0000001 Sucle-time 3120kHz Sucle-time 333ns, wraps every 68905062459ns Thu Jan 10:00:261970 kem info kemel: 0.000001 Sucle-time 3120kHz Sucle-time 212555535 Thu Jan 10:00:261970 kem info kemel: 0.000001 Sucle-time 3120kHz Sucle-time 333ns, wraps every 689050624599ns Thu Jan 10:00:261970 kem info kemel: 0.	Thu Jan 1 00:00:2	6 1970 kem	notice kernel:	1000000.0	lowmem : 0xcl	0000000 - 0xc400000	(64 MB)		
Thu Jan 1 100 00.26 1970 kem notice kemel; 0.000000 text: 0xc0003000 - 0xc062054 (6244 46) Thu Jan 1 00 00 26 1970 kem notice kemel; 0.000000 int: 0xc0621000 - 0xc062000 (188 kB) Thu Jan 1 00 00 26 1970 kem notice kemel; 0.000000 int: 0xc0621000 - 0xc062000 (188 kB) Thu Jan 1 00 00 26 1970 kem notice kemel; 0.000000 int: 0xc0621000 - 0xc06005000 - 0xc06005000 (188 kB) Thu Jan 1 00 00 26 1970 kem info kemel; 0.000000 its: 0xc069c724 - 0xc0600586 (253 kB) Thu Jan 1 00 00 26 1970 kem info kemel; 0.000000 Dist: HVAign=22, Order=0-3, MinObject=0, CPUs=1, Nodes=1 Thu Jan 1 00 00 26 1970 kem info kemel; 0.000000 Notestiment of teaf fanout to 32. Thu Jan 1 00 00 26 1970 kem info kemel; 0.000000 Notestource: nuce800-timer5: mask: 0xffffffffffffffffffffffffffffffffffff	Thu Jan 1 00:00:2	6 1970 kem	notice kernel	10000000	modules : 0xbf	0000000 - 0xc0000000	(16 MB)		
Thu Jan 1 0.00.26 1970 kem notice keme! 0.000001 init: 0xc0621000 - 0xc065000 (138 kB) Thu Jan 1 0.00.26 1970 kem notice keme! 0.0000001 bas: 0xc065000 - 0xc069c784 (306 kB) Thu Jan 1 0.00.26 1970 kem notice keme! 0.0000001 bas: 0xc069c784 (306 kB) Thu Jan 1 0.00.26 1970 kem info keme! 0.0000001 bus: 0xc069c784 (306 kB) Thu Jan 1 0.00.26 1970 kem info keme! 0.0000001 bus: 0xc069c784 (306 kB) Thu Jan 1 0.00.26 1970 kem info keme! 0.0000001 bulle-time adjustment of leaf fanout to 32. Thu Jan 1 0.00.26 1970 kem info keme! 0.0000001 clobel-timer5: mask: 0xfffff max_cycles 0xfffff, max_idle_ns: 62215505635 ns Thu Jan 1 0.00.26 1970 kem info keme! 0.000001 clobel-timer5: mask: 0xfffff max_cycles 0xfffff, max_idle_ns: 62215505635 ns Thu Jan 1 0.00.26 1970 kem info keme! 0.000001 clobel-timer5: mask: 0xfffff max_cycles 0xfffff, max_idle_ns: 62215505635 ns Thu Jan 1 0.00.26 1970 kem info keme! 0.000001 clobel-timer5: mask: 0xffffff max_cycles 0xfffff Thu Jan 1 0.00.26 1970 kem info keme! 0.000001 clobel-timer5 max_cycles 0xfffff max_cycles 0xff	Thu Jan 1 00:00:2	6 1970 kem	notice kernel:	1000000.0	text : 0xc000	8000 - 0xc0620f54 (6	244 kB)		
Thu Jan 100 00 26 1970 kem notice kemel; 0.0000001 data: 0xc065000 - 0xc069c724 (306 kB) Thu Jan 100 00 26 1970 kem info kemel; 0.0000001 bs:: 0xc0660784 - 0xc0660784 - 0xc0660784 (306 kB) Thu Jan 100 00 26 1970 kem info kemel; 0.0000001 Preemptible hierarchical RCU implementation. Thu Jan 100 00 26 1970 kem info kemel; 0.0000001 Preemptible hierarchical RCU implementation. Thu Jan 100 00 26 1970 kem info kemel; 0.0000001 Preemptible hierarchical RCU implementation. Thu Jan 100 00 26 1970 kem info kemel; 0.0000001 Preemptible hierarchical RCU implementation. Thu Jan 100 00 26 1970 kem info kemel; 0.0000001 Preemptible hierarchical RCU implementation. Thu Jan 100 00 26 1970 kem info kemel; 0.0000001 Preemptible hierarchical RCU implementation. Thu Jan 100 00 26 1970 kem info kemel; 0.000001 Preemptible hierarchical RCU implementation. Thu Jan 100 00 26 1970 kem info kemel; 0.000001 Preemptible hierarchical RCU implementation. Thu Jan 100 00 26 1970 kem info kemel; 0.1000741 Console: color dummy device 80x30 Thu Jan 100 00 26 1970 kem info kemel; 0.1860161 console [ttyS0] enabled 118601	Thu Jan 1 00:00:2	6 1970 kem	notice kernel	10000000	init 0xc0621	000 - 0xc0650000 (188 kB)		
Thu Jan 1 0002.05 1970 kem notice kemel: 0.000001 bss: 0xc0960704 - 0xc09d0876 (258 bd) Thu Jan 1 0002.26 1970 kem info kemel: 0.000001 bliel HW3igm 232, Orden-0.3 Minobigista=0, OFUs=1, Nodes=1 Thu Jan 1 0002.26 1970 kem info kemel: 0.000001 bliel HW3igm 232, Orden-0.3 Minobigista=0, OFUs=1, Nodes=1 Thu Jan 1 0002.26 1970 kem info kemel: 0.0000001 bliel Hierarchical RCU implementation. Thu Jan 1 0000.26 1970 kem info kemel: 0.000001 bliel Hierarchical RCU implementation. Thu Jan 1 0000.26 1970 kem info kemel: 0.0000001 bliel Hierarchical RCU implementation. Thu Jan 1 0000.26 1970 kem info kemel: 0.000001 closeburg closebur	Thu Jan 1 00:00:2	6 1970 kem	notice kernel:	1000000.0	data : 0xc065	50000 - 0xc069c784	306 kB)		
Thu Jan 1 00 02 36 1970 kem info kernel: 0 0000000 SLUB: HValign=32, Order=0-3. MinObjects=0, CPUs=1, Nodes=1 Thu Jan 1 00 00 26 1970 kem info kernel: 0 000000 Preemplible hierarchical RCU implementation. Thu Jan 1 00 00 26 1970 kem info kernel: 0 000000 Preemplible hierarchical RCU implementation. Thu Jan 1 00 00 26 1970 kern info kernel: 0 000000 NE_IRQS:545 Thu Jan 1 00 00 26 1970 kern info kernel: 0 000001 NE_IRQS:545 Thu Jan 1 00 00 26 1970 kern info kernel: 0 000071 Console: color dummy device 80x30 Thu Jan 1 00 00 26 1970 kern info kernel: 0 000071 Console: color dummy device 80x30 Thu Jan 1 00 00 26 1970 kern info kernel: 0 1000741 Console: color dummy device 80x30 Thu Jan 1 00 00 26 1970 kern info kernel: 0 1000741 Console: color dummy device 80x30 Thu Jan 1 00 00 26 1970 kern info kernel: 0 1000741 Console: color dummy device 80x30 Thu Jan 1 00 00 26 1970 kern info kernel: 0 180071 Calibrating delay loop (skipped) preset value 148.88 BogoMIPS (lpj=744448) Thu Jan 1 00 00 26 1970 kern info kernel: 0 209708 IM ounchiont-cache hash table entries: 1024 (order: 0, 4096 bytes) Thu Jan 1 00 00 26 1970 kern info kernel: 0 209708 IM ounchiont-cache hash table entries: 1024 (order: 0, 4096 bytes) Thu Jan 1 00 00 26 1970 kern info kernel: 0 209708 IM ounchiont-cache hash table entries: 1024 (order: 0, 4096 bytes)	Thu Jan 1 00:00:2	6 1970 kem	notice kernel	10000000	bss 0xc069	c784 - 0xc06db8f8 (253 kB)		
Thu Jan 1 00 00 26 1970 kem info kernel 0 000000) Preemptible hierarchical RCU implementation. Thu Jan 1 00 00 26 1970 kem info kernel 0 000000) Publickiew adjustment of leaf fanout to 32. Thu Jan 1 00 00 26 1970 kem info kernel 0 000000) Build-line adjustment of leaf fanout to 32. Thu Jan 1 00 00 26 1970 kem info kernel 0 000003 clocksource. nuc980-liner5: mask: 0xfffff max_cycles: 0xfffff, max_idle_ns: 62215505635 ns Thu Jan 1 00 00 26 1970 kem info kernel 0 000033 sched_clock: 24 bits at 120kHz; resolution 333ns, wraps every 69905062439ns Thu Jan 1 00 00 26 1970 kem info kernel 0 000031 sched_clock: 24 bits at 120kHz; resolution 833ns, wraps every 69905062439ns Thu Jan 1 00 00 26 1970 kem info kernel 0 19000911 Calibrating delay loop (skipped) preset value 148 88 BogoMIPS (lpj=744448) Thu Jan 1 00 00 26 1970 kem info kernel 0 19001911 Calibrating delay loop (skipped) preset value 148 88 BogoMIPS (lpj=744448) Thu Jan 1 00 00 26 1970 kem info kernel 0 209708 Mount-cache hash table entries: 1024 (order: 0, 4096 bytes) Thu Jan 1 00 00 26 1970 kem info kernel 0 209708 Mount-cache hash table entries: 1024 (order: 0, 4096 bytes) Thu Jan 1 00 00 26 1970 kem info kernel 0 2249833 Setting up sablic delay downer; resolution max_ycide: 0.4096 bytes) Thu Jan 1 00 00 26 1970 kem info kernel 0 2249833 Setting up sablic delay downer; resolution resoluting upaskic delay downer; resolution resolution r	Thu Jan 1 00:00:2	6 1970 kem	info kernel: [0.0000001 SL	UB: HWalion=32	Order=0-3. MinObiec	ts=0. CPUs=1. M	Nodes=1	
Thu Jan 1 00:02:6 1970 kem info kernet 0 0000000 Build-time adjustment of leaf fanout to 32. Thu Jan 1 00:02:6 1970 kem info kernet 0 0000000 NP_IRQS:545 Thu Jan 1 00:02:6 1970 kem info kernet 0 0000001 NP_IRQS:545 Thu Jan 1 00:02:6 1970 kem info kernet 0 0000001 NP_IRQS:545 Thu Jan 1 00:02:6 1970 kem info kernet 0 0000001 clocksource: nuc880-timer5: mask: 0xffffff max_cycles: 0xffffff, max_idle_ns: 62215505635 ns Thu Jan 1 00:02:6 1970 kem info kernet 0 0000741 Clocksource: nuc880-timer5: mask: 0xffffff max_cycles: 0xffffff, max_idle_ns: 62215505635 ns Thu Jan 1 00:02:6 1970 kem info kernet 0 1686161 console: colour dummy device 80x30 Thu Jan 1 00:02:6 1970 kem info kernet 0 1690511 clicitarating delay loop (skipped) preset value. 148:88 BogoMIPS (lpj=744448) Thu Jan 1 00:02:6 1970 kem info kernet 0 19891741 pld_max: default: 32768 minimum: 301 Thu Jan 1 00:02:6 1970 kem info kernet 0 209708 (Mount-cache hash table entries: 1024 (order: 0, 4096 bytes) Thu Jan 1 00:02:6 1970 kem info kernet 0 2189161 CPU: Testing write buffer coherency: ck Thu Jan 1 00:02:6 1970 kem info kernet 0 22149813 Setting up stabic identify map for X4400- 0x4326 Thu Jan 1 00:02:6 1970 kem info kernet 0 22149813 Setting up stabic identify map for X4400- 0x4326 Thu Jan 1 00:02:6 1970 kem info kernet 0 22321616 (truck shash table entrify max_cycles: 1, 1,	Thu Jan 1 00:00:2	6 1970 kem	info kernel: [0 0000001 Pr	eemptible hierarc	hical RCU implementa	tion		
Thu Jan 1 00 00 26 1970 kem info kernet 0 000000) NE_IRQS:54 Thu Jan 1 00 00 26 1970 kem info kernet 0 000000] clocksource: nuc080-liner5: mask: 0xfffff max_cycles: 0xfffff, max_idle_ns: 62215505635 ns Thu Jan 1 00 00 26 1970 kem info kernet 0 000000] clocksource: nuc080-liner5: mask: 0xfffff max_cycles: 0xfffff, max_idle_ns: 62215505635 ns Thu Jan 1 00 00 26 1970 kem info kernet 0 000001] clocksource: nuc080-liner5: mask: 0xfffff max_cycles: 0xfffff, max_idle_ns: 62215505635 ns Thu Jan 1 00 00 26 1970 kem info kernet 0 0000711 Console: colour dummy device 80x30 Thu Jan 1 00 00 26 1970 kem info kernet 0 196061 [thy50] enabled Thu Jan 1 00 00 26 1970 kem info kernet 0 198074 [nmax: default 32768 minimum: 301 Thu Jan 1 00 00 26 1970 kem info kernet 0 209708 [Mount-cache hash table entries: 1024 (order: 0, 4096 bytes) Thu Jan 1 00 00 26 1970 kern info kernet 0 209708 [Mount-cache hash table entries: 1024 (order: 0, 4096 bytes) Thu Jan 1 00 00 26 1970 kern info kernet 0 218916 [CPU: Testing write buffer coherency: ok Thu Jan 1 00 00 26 1970 kern info kernet 0 2246933 Setting up stabic identify max_cycles: 0xffffff max_cycles Thu Jan 1 00 00 26 1970 kern info kernet 0 2246933 Setting up stabic identify max_cycles Thu Jan 1 00 00 26 1970 kern info kernet 0 2246933 Setting up stabic identify max_cycles Thu Jan 1 00 00 2	Thu Jan 1 00:00:2	6 1970 kem	info kernel: [0.0000001	Build-time adjust	tment of leaf fanout to	32		
Thu Jan 1 00:00:26 1970 kem info kernet 0 000000] clocksource: nuc980-timer5: mask: 0xffffff max_cycles: 0xfffff, max_idle_ns: 62215505635 ns Thu Jan 1 00:00:26 1970 kem info kernet 0 000033 sched_clock: 24 bits at 120kHz, resolution 333ns, wraps every 69905062489ns Thu Jan 1 00:00:26 1970 kem info kernet 0 1000741 Consolie: colour dummy device 80x30 Thu Jan 1 00:00:26 1970 kern info kernet 0 1805616 console: colour dummy device 80x30 Thu Jan 1 00:00:26 1970 kern info kernet 0 1900912 clinitaring delay loop (skipped) preset value 148.88 BogoMIPS (lpj=744448) Thu Jan 1 00:00:26 1970 kern info kernet 0 1900912 clinitaring delay loop (skipped) preset value 148.88 BogoMIPS (lpj=744448) Thu Jan 1 00:00:26 1970 kern info kernet 0 1900912 clinitaring delay loop (skipped) preset value 148.88 BogoMIPS (lpj=744448) Thu Jan 1 00:00:26 1970 kern info kernet 0 1900912 clinitaring delay loop (skipped) preset value 148.88 BogoMIPS (lpj=744448) Thu Jan 1 00:00:26 1970 kern info kernet 0 203133 Mount-cache hash table entries: 1024 (order: 0, 4096 bytes) Thu Jan 1 00:00:26 1970 kern info kernet 0 2189161 CPU: Testing write buffer coherency: ok Thu Jan 1 00:00:26 1970 kern info kernet 0 2214831 Setting up static identify map for 0x8400- 0x8432 Thu Jan 1 00:00:26 1970 kern info kernet 0 2282163 (Dicksource: infies: mask: Oxffffff max_cycles: Oxffffff, max_idle_ns: 19112604462750000 ns Thu Jan 1 00:00:26 1970 ker	Thu Jan 1 00:00:2	6 1970 kem	info kernel: [0 0000001 NE	R IROS:545				
Thu Jan 1 00:02:6:1970 kem.info kernet; 0.0000333 sched_clock: 24 bits at 120kHz, resolution 8333ns, wraps every 69905062489ns Thu Jan 1 00:02:6:1970 kem.info kernet; 0.000741 Console: colour dummy device 80x30 Thu Jan 1 00:02:6:1970 kem.info kernet; 0.1968f16 console (Ity/S0 enabled Thu Jan 1 00:02:6:1970 kem.info kernet; 0.1968f16 console (Ity/S0 enabled Thu Jan 1 00:02:6:1970 kern.info kernet; 0.1987f1 Jaintarång delay loop (skipped) preset value 148.88 BogoMIPS (Ipj=744448) Thu Jan 1 00:02:6:1970 kern.info kernet; 0.2081733 Mount-cache hash table entries: 1024 (order: 0.4996 bytes) Thu Jan 1 00:02:6:1970 kern.info kernet; 0.209708 (Mount-cache hash table entries: 1024 (order: 0.4996 bytes) Thu Jan 1 00:02:6:1970 kern.info kernet; 0.209708 (Mount-cache hash table entries: 1024 (order: 0.4996 bytes) Thu Jan 1 00:02:6:1970 kern.info kernet; 0.2189716 (CPU: Testing write buffer coherency: ok Thu Jan 1 00:02:6:1970 kern.info kernet; 0.2249833 Setting up static identify map for 0x8400 - 0x8430 Thu Jan 1 00:02:6:1970 kern.info kernet; 0.2248313 bits mask: 256 (order: -1, 3072 bytes) Thu Jan 1 00:02:6:1970 kern.info kernet; 0.228316 [tatex hash table entries: 256 (order: -1, 3072 bytes) Thu Jan 1 00:02:6:1970 kern.info kernet; 0.288374 jincht ore: initialized pincht subsystem Thu Jan 1 00:02:6:1970 kern.info kerne	Thu Jan 1 00:00:2	6 1970 kem	info kernel: [0.0000001 clo	cksource: nuc98	0-timer5: mask: 0xffffff	max_cvcles: 0xt	fffff, max idle ns: 622155	05635 ns
Thu Jan 100:02:6 1970 kem info kernet 0.0007411 Console: colour dummy device 30x30 Thu Jan 100:02:6 1970 kem info kernet 0.168616 (console (tty)50 (enabled Thu Jan 100:02:6 1970 kem info kernet 0.168616 (console (tty)50 (enabled Thu Jan 100:02:6 1970 kem info kernet 0.199074 (tty) (tty) Thu Jan 100:02:6 1970 kem info kernet 0.2091708 (tty) Thu Jan 100:02:6 1970 kem info kernet 0.2091708 (tty) 100:02:6 1970 kem info kernet 0.211916 (CPU. Testing virite buffer coherency: ck Thu Jan 100:02:6 1970 kem info kernet 0.2214933 (tty) 100:02:6 1970 kem info kernet 0.2214933 (tty) 100:02:6 1970 kem info kernet 0.2214933 (tty) 100:02:6 1970 kern info kernet 0.2214933 (tty) 100:02:6 1970 kern info kernet 0.2214933 (tty) 100:02:6 1970 kern info kernet 0.221593 (tty) <td>Thu Jan 1 00:00:2</td> <td>6 1970 kem</td> <td>info kernel: [</td> <td>0.0000331 scl</td> <td>hed clock: 24 bits</td> <td>at 120kHz, resolution</td> <td>8333ns, wraps</td> <td>every 69905062489ns</td> <td></td>	Thu Jan 1 00:00:2	6 1970 kem	info kernel: [0.0000331 scl	hed clock: 24 bits	at 120kHz, resolution	8333ns, wraps	every 69905062489ns	
Thu Jan 1 0002:05 1970 kem info kemet 0 168616j console (tty 50) enabled Thu Jan 1 00:002:05 1970 kem info kemet 0 169011 Calibrating delay loop (skipped) preset value 148.88 BogoMIPS (lpj=744448) Thu Jan 1 00:002:05 1970 kem info kemet 0 159174 jpi_max: default 32768 minimum: 301 Thu Jan 1 00:002:05 1970 kem info kemet 0 203133 Mount-cache hash table entries: 1024 (order: 0, 4096 bytes) Thu Jan 1 00:002:05 1970 kem info kemet 0 203033 Mount-cache hash table entries: 1024 (order: 0, 4096 bytes) Thu Jan 1 00:002:05 1970 kem info kemet 0 209708 (Mount)coint-cache hash table entries: 1024 (order: 0, 4096 bytes) Thu Jan 1 00:002:05 1970 kem info kemet 0 209708 (Mount)coint-cache hash table entries: 1024 (order: 0, 4096 bytes) Thu Jan 1 00:002:05 1970 kem info kemet 0 218916 (CPU: Testing write buffer coherency: ok Thu Jan 1 00:002:05 1970 kem info kemet 0 2248933 Setting up static identify map for 0x4040 - 0x432 Thu Jan 1 00:002:05 1970 kem info kemet 0 2248933 Setting up static identify map for 0x4040 - 0x432 Thu Jan 1 00:002:05 1970 kem info kemet 0 282316 (futex hash table entries: 256 (order: -1, 3072 bytes) Thu Jan 1 00:002:26 1970 kem info kemet 0 282813 (futex hash table entries: 256 (order: -1, 3072 bytes) Thu Jan 1 00:002:26 1970 kem info kemet 0 2826133 NET. Registered protocol family 16 Thu Jan 1 00:002:26 1970 kem info kemet 0 293	Thu Jan 1 00:00:2	6 1970 kem	info kernel: [0.0007411 Co	nsole: colour dur	nmv device 80x30			
Thu Jan 1 00:02:6 1970 kem info kernet [0 1900911 Calibrating delay loop (skipped) preset value. 148.88 BogoMIPS (jpj=744448) Thu Jan 1 00:02:6 1970 kem info kernet [0 198174 jpj=736 minimum: 301 Thu Jan 1 00:02:6 1970 kem info kernet [0 209708 jMount-eache hash table entries: 1024 (order: 0, 4096 bytes) Thu Jan 1 00:02:6 1970 kem info kernet [0 209708 jMount-eache hash table entries: 1024 (order: 0, 4096 bytes) Thu Jan 1 00:02:6 1970 kem info kernet [0 209708 jMount-eache hash table entries: 1024 (order: 0, 4096 bytes) Thu Jan 1 00:02:6 1970 kem info kernet [0 209708 jMount-eache hash table entries: 1024 (order: 0, 4096 bytes) Thu Jan 1 00:02:6 1970 kem info kernet [0 215916 jCPU: Testing vrite buffer coherency: ok Thu Jan 1 00:02:6 1970 kem info kernet [0 2215051 clockource; jiffes: mask: 0xffffffffftmac_cycicks: 0x6430 Thu Jan 1 00:02:6 1970 kem info kernet [0 2215051 clockource; jiffes: anak: 0xffffffftmac_cycicks: 0xffffff, max_cycicks:	Thu Jan 1 00:00:2	6 1970 kem	info kernel: [0.1866161 co	nsole [ttyS0] enal	bled			
Thu Jan 1 00 00:26 1970 kem info kemel; 0 1993741 pid_max: default: 32768 mininuum: 301 Thu Jan 1 00:00:26 1970 kem info kemel; 0 203133 Mount-cache hash table entries: 1024 (order: 0, 4096 bytes) Thu Jan 1 00:00:26 1970 kem info kemel; 0 203133 Mount-cache hash table entries: 1024 (order: 0, 4096 bytes) Thu Jan 1 00:00:26 1970 kem info kemel; 0 218916 CPU: Testing write buffer coherency: ok Thu Jan 1 00:00:26 1970 kem info kemel; 0 224893 Setting up static identity map for 0x4800- 0x843c Thu Jan 1 00:00:26 1970 kem info kemel; 0 224893 Setting up static identity map for 0x4800- 0x843c Thu Jan 1 00:00:26 1970 kem info kemel; 0 224893 Setting up static identity map for 0x4800- 0x843c Thu Jan 1 00:00:26 1970 kem info kemel; 0 224893 Setting up static identity map for 0x4800- 0x843c Thu Jan 1 00:00:26 1970 kem info kemel; 0 226316 (futke hash table entries: 256 (order: -1, 3072 bytes) Thu Jan 1 00:00:26 1970 kem info kemel; 0 288674 (pincht core: initialized pincht subsystem Thu Jan 1 00:00:26 1970 kem info kemel; 0 288673 (pincht core: initialized pincht subsystem Thu Jan 1 00:00:26 1970 kem info kemel; 0 289631 NET. Registered protocol farmily 16 Thu Jan 1 00:00:26 1970 kem info kemel; 0 303199 DMA: preallocated 256 KB pool for atomic coherent allocations	Thu Jan 1 00:00:2	6 1970 kem	info kernel: [0.1900911 Ca	librating delay log	op (skipped) preset val	ue. 148.88 Bog	oMIPS (Ipi=744448)	
Thu Jan 1 00:00:26 1970 kem info kernet; 0.203133 [Mount-cache hash table entries: 1024 (order: 0.4096 bytes) Thu Jan 1 00:00:26 1970 kem info kernet; 0.203703 [Mount-cache hash table entries: 1024 (order: 0.4096 bytes) Thu Jan 1 00:00:26 1970 kem info kernet; 0.203703 [Mountpoint-cache hash table entries: 1024 (order: 0.4096 bytes) Thu Jan 1 00:00:26 1970 kem info kernet; 0.2184163 [CPU: Testing write buffer coherency: ok Thu Jan 1 00:00:26 1970 kem info kernet; 0.224983] Setting up static identify map for 0x8400 - 0x8436 Thu Jan 1 00:00:26 1970 kem info kernet; 0.221556] clocksource; jiffies: mask: 0xfiffffff max_cycles: 0xfiffffff, max_idle_ns: 19112604462750000 ns Thu Jan 1 00:00:26 1970 kem info kernet; 0.228674] jointch core: infailaced pinctf subsystem Thu Jan 1 00:00:26 1970 kern info kernet; 0.288674] pincth core: infailaced pinctf subsystem Thu Jan 1 00:00:26 1970 kern info kernet; 0.298674] pincth core: infailaced pinctf subsystem Thu Jan 1 00:00:26 1970 kern info kernet; 0.298674] pincth core: infailaced pinctf subsystem Thu Jan 1 00:00:26 1970 kern info kernet; 0.298674] pincth, preallocated 255 KiB pool for atomic coherent allocations	Thu Jan 1 00:00:2	6 1970 kem	info kernel: [0.1981741 pid	max default 32	2768 minimum: 301			
Thu Jan 1 00:00:26 1970 kem.info kernet; 0 209708 [Mountpoint-cache hash table entries: 10:24 (order: 0, 4096 bytes) Thu Jan 1 00:00:26 1970 kem.info kernet; 0 219916 [CPU: Testing write buffer coherency: ok Thu Jan 1 00:00:26 1970 kern.info kernet; 0 224893 [Setting up static identify map for 0x400- 0x843c Thu Jan 1 00:00:26 1970 kern.info kernet; 0 224893 [Setting up static identify map for 0x400- 0x843c Thu Jan 1 00:00:26 1970 kern.info kernet; 0 202316 [Utex hash table entries: 256 (order: -1, 3072 bytes) Thu Jan 1 00:00:26 1970 kern.info kernet; 0 282316 [Utex hash table entries: 256 (order: -1, 3072 bytes) Thu Jan 1 00:00:26 1970 kern.info kernet; 0 28483 [Setting app intialized pincht subsystem Thu Jan 1 00:00:26 1970 kern.info kernet; 0 284633 [NET. Registered protocol family 16 Thu Jan 1 00:00:26 1970 kern.info kernet; 0 236193 [NET. Registered protocol family 16 Thu Jan 1 00:00:26 1970 kern.info kernet; 0 236193 [NET. Registered protocol family 16	Thu Jan 1 00:00:2	6 1970 kem	info kernel: [0.2031331 Md	unt-cache hash t	able entries: 1024 (or	ler: 0. 4096 byte	s)	
Thu Jan 1 00:02:6 1970 kem info kernet [0 2:18916] CPU: Testing write buffer coherency: ok hu Jan 1 00:02:6 1970 kem info kernet [0 2:24693] Setting up static identify map for 0x4400- 0x443c hu Jan 1 00:02:6 1970 kem info kernet [0 2:24693] Setting up static identify map for 0x4400- 0x443c hu Jan 1 00:02:6 1970 kem info kernet [0 2:82316] futex hash table entries: 256 (order: -1, 3072 bytes) hu Jan 1 00:00:26 1970 kem info kernet [0 2:88674] pinct core: inflatic eq pinct subsystem hu Jan 1 00:00:26 1970 kem info kernet [0 2:88674] pinct core: inflatic eq pinct subsystem hu Jan 1 00:00:26 1970 kem info kernet [0 2:98674] pinct core: inflatic eq pinct subsystem hu Jan 1 00:00:26 1970 kem info kernet [0 2:98674] pinct, prealocated 2:56 KiB pool for atomic coherent allocations	Thu Jan 1 00:00:2	6 1970 kem	info kernel: [0.2097081 Md	ountpoint-cache h	ash table entries: 102-	4 (order: 0, 4096	bytes)	
Thu Jan 1 00:00:26 1970 kem.info kernet: [0.224983] Setting up static identity map for 0x8400 - 0x843c Thu Jan 1 00:00:26 1970 kern info kernet: [0.271558] clocksource: jiffies: mask: 0xffffff max_codes: 0xffffff, max_jidle_ns: 19112604462750000 ns Thu Jan 1 00:00:26 1970 kern info kernet: [0.282316] futer, tashs table entries: 256 (order: -1, 3072 bytes) Thu Jan 1 00:00:26 1970 kern info kernet: [0.28841 pincht core: initialized pincht subsystem Thu Jan 1 00:00:26 1970 kern info kernet: [0.296433] NET: Registered protocol family 16 Thu Jan 1 00:00:26 1970 kern info kernet: [0.303199] DIAA: preallocated 256 KIB pool for atomic coherent allocations	Thu Jan 1 00:00:2	6 1970 kem	info kernel: [0.2189161 CF	U: Testing write t	ouffer coherency: ok			
hu Jan 1 00:00:26 1970 kem info kernet [0 2271558] clocksource: jiffles: mask: 0:#ffffff max_cycles: 0:#fffffff max_cycles: 0:#ffffff max_cycles: 0:#ffffff max_cycles: 0:#ffffff max_cycles: 0:#fffffff max_cycles: 0:#fffffffffffffffffffffffffffffffffff	hu Jan 1 00:00:2	6 1970 kem	info kernel: [0.2249831 Se	tting up static ide	ntity map for 0x8400 -	0x843c		
Thu Jan 1 00:00:26 1970 kem info kernel: [0.282316] futex hash table entries: 256 (order: -1, 3072 bytes) Thu Jan 1 00:00:26 1970 kem info kernel: [0.288374] pincht core: initialized pincht subsystem Thu Jan 1 00:00:26 1970 kem info kernel: [0.296433] NET: Registered protocol family 16 Thu Jan 1 00:00:26 1970 kem info kernel: [0.303199] DMA: preallocated 256 KiB pool for atomic coherent allocations	Thu Jan 1 00:00:2	6 1970 kem	info kernel: [0.271558] clo	cksource: jiffies:	mask: 0xffffffff max cy	cles: 0xffffffff, ma	ax idle ns: 191126044627	750000 ns
hu Jan 1 00:00:26 1970 kem info kemel: [0 228874] pincht core: initialized pincht subsystem hu Jan 1 00:00:26 1970 kem info kemel: [0 296433] NET. Registered protocol family 16 hu Jan 1 00:00:26 1970 kem info kemel: [0 303199] DMA: preallocated 256 KiB pool for atomic coherent allocations	Thu Jan 1 00:00:2	6 1970 kem	info kernel: [0.2823161 fut	ex hash table ent	ries: 256 (order: -1. 30	72 bytes)		
'hu Jan 1 00:00:26 1970 kern info kernel: [0.296433] NET: Registered protocol family 16 'hu Jan 1 00:00:26 1970 kern info kernel: [0.303199] DMA: preallocated 256 KiB pool for atomic coherent allocations	Thu Jan 1 00:00:2	6 1970 kem	info kernel: [0.2888741 pin	ctrl core: initialize	ed pinctrl subsystem			
hu Jan 1 00:00:26 1970 kem info kernel: [0.303199] DMA: preallocated 256 KiB pool for atomic coherent allocations	Thu Jan 1 00:00:2	6 1970 kem	info kernel: [0.2964331 NE	T: Registered pro	tocol family 16			
	Thu Jan 1 00:00:2	6 1970 kem	info kernel: [0.3031991 DM	A: preallocated	256 KiB pool for atomic	c coherent alloca	ations	
hu Jan 1 00:00:26 1970 kem.info kernel: [0.316783] <dt> nuc980 dt device init +</dt>	Thu Jan 1 00:00:2	6 1970 kem	info kernel: [0.3167831 <d< td=""><td>T> nuc980 dt de</td><td>evice_init +</td><td></td><td></td><td></td></d<>	T> nuc980 dt de	evice_init +			
hu Jan 1 00:00:26 1970 kem info kernel [0.348016] <dt> nuc980 dt device init -</dt>	Thu Jan 1 00:00:2	6 1970 kem	info kernel: [0.3480161 <0	T> nuc980 dt de	evice init -			

Status > Kernel Log



BL200UA s

[6.90000] Booling Linux on physical CPU 0:0 (c) 000000] CPU. ARM926E-5 [41069269] (pcc version 5.4.0 (LEDE GCC 5.4.0 unknown)) #0 PREEMPT Sat May 9 15:23:54.2020 (0.000000] CPU. ARM926E-5 [41069269] (pcc version 5.4.0 (LEDE GCC 5.4.0 unknown)) #0 PREEMPT Sat May 9 15:23:54.2020 (0.000000] Machine model: Nuvrion NUUCS80 107-CateWay Version: 0.1 (0.000000] CPU. VTX data cache verbanck. (0.000000] CPU. VTX data cache verbanck. (0.000000] Normal Zone: 128 pages used for mermap (0.000000] Normal Zone: 128 pages used for mermap (0.000000] Normal Zone: 128 pages used for mermap. (0.000000] Normal Zone: 128 pages used for mermap. (0.000000] Normal Zone: 1278 042768 us2768 user 132788 (0.000000] Popu-allice: 01 of 42758 us2768 user 132788 (0.000000] Denzi cache verbanck. (0.000000] Normal Zone: 128 pages used for mermap. (0.000000] Normal Zone: 128 pages used for mermap. (0.000000] Normal Zone: 128 pages used for mermap. (0.000000] Normal Zone: 128 pages used for console-thy50.11520008 rdml=/sbin/int mem=64M lpj=744448 (0.000000] Denky cache hash table entites. 3092 (order: 2, 1324 bytes) (0.000000] Denky cache hash table entites. 3092 (order: 2, 32768 bytes) (0.000000] Denky cache hash table entites. 3092 (order: 2, 32768 bytes) (0.000000] Memory 57756/63556 valiable (4536 K kemel cache.305K rindata, 1704K rodata, 186K init, 252K bss, 7780K reserved, 0K cma-reserved) (0.000000] Visual Kemel mermony layout (0.000000] Namalo: DecAgoboondo - Mr8000000 (3072 kB) (0.000000] Namalo: DecAgoboondo - Mr8000000 (44 MB) (0.000000] Memalo: 50x6000000 - 0xc4000000 (44 MB) (0.000000] Namalo: DecAgoboondo - Mr800000 (458 kB) (0.000000] Suid-Kima adjustment of leaf fancut to 3	K	Kernel Log
0 0000000 Linux 'ension 4.4 194 (peng@peng) (gcc version 5.40 (LEDE GCC 5.4 0 unknown) #0 PREEMPT Sat May 9 15.23.54 2020 0.000000 CPU_VIVT data cache, VIVT instruction cache 0.000000 Composition cache Com	ſ	0.000000] Booting Linux on physical CPU 0x0
[0.00000] CPU: ARM20ED-S [41062265] revision 5 (ARM-STEJ), cr=0005317' [0.000000] Machine model: Nuvoton NUCS90 IOT-GateWay Version: 0.1 [0.000000] On mode 10 tolapages: 15334 [0.000000] On mode 10 tolapages: 15334 [0.000000] Normal zone: 0 pages reserved [0.000000] Normal zone: 0 pages reserved [0.000000] Normal zone: 10 pages reserved [0.000000] Normal zone: 0 pages reserved [0.000000] Normal zone: 10 pages reserved [0.000000] Depu-alloc: (0 10 Good Constraints' 256 (order - 2, 1032 4 bytes) [0.000000] Ontrol pack table entries: 358 (order - 3, 32768 bytes) [0.000000] Ontrol cock hash table entries: 358 (order - 3, 32768 bytes) [0.000000] Vector: 100000 - 0.0000000 (14 kB) [0.000000] Vector: 100000 - 0.0000000 (14 kB) [0.000000] Vector: 100000 - 0.0000000 (14 kB) [0.000000] Vector: 100000 - 0.00000000 (16 kB) [0.000000] Unical EvectAl0000 - 0.00000000 (16 kB) [0.000000] Inst: 0xc600000 - 0.00000000 (16 kB) [0.000000] Inst: 0xc600000 - 0.00000000 (16 kB) [0.000000] Unical EvectAl0000 - 0.00000000 (16 kB) [0.000000] Unical EvectAl000 - 0.00000000 (16 kBB) [0.0000000] Unical EvectAl0000 - 0.00000000 (16 kBB)	ī	0.000000] Linux version 4.4.194 (peng@peng) (gcc version 5.4.0 (LEDE GCC 5.4.0 unknown)) #0 PREEMPT Sat May 9 15:23:54 2020
0.000000 CPU: VIVT atta cache, VIVT instruction cache 0.000000 Memory policy: Data cache writeback 0.000000 Memory policy: Data cache writeback 0.000000 node 0 fotalgases: 16334 0.000000 Nomal zone: 129 pages used 0 normempap 0.000000 Normal zone: 129 pages used 0 normempap 0.000000 Normal zone: 1394 pages. LFO bath: 3 0.000000 Normal zone: 1394 pages. LFO bath: 3 0.0000000 Normal zone: 1394 pages. LFO bath: 3 0.0000000 Normal zone: 1394 pages. LFO bath: 3 0.0000000 Normal zone: 1395 (order : 2, 1024 bytes) 0.0000000 Publicy ache hash table entries: 256 (order : 2, 1034 bytes) 0.0000000 Inder zone: rob-Neithy Mitoblock2 console-thyso. 115200R zintal: 130K init, 252K bas, 7780K reserved. 0K cma-reserved) 0.0000000 Netral Esc (order : 3, 13768 bytes) 0.0000000000 Netral Esc (order : 2, 16334 bytes) 0.0000000 Netral Esc (order : 3, 37268 bytes) 0.0000000 Netral Esc (order : 3, 37268 bytes) 0.0000000 Netral Esc (order : 3, 13768 bytes) 0.0000000 Netral Esc (order : 4, 1438 bytes) 0.0000000 <t< td=""><td>I</td><td>0.000000] CPU: ARM926EJ-S [41069265] revision 5 (ARMv5TEJ), cr=0005317f</td></t<>	I	0.000000] CPU: ARM926EJ-S [41069265] revision 5 (ARMv5TEJ), cr=0005317f
[0.000000] Machine model: Nuvoton NUCS90 107-GateVky Version: 0.1 0.000000] On mode 0 totalpages: 16334 0.000000] On mode 0 totalpages: 16334 0.000000] Normal zone: 0.pages reserved 0.000000] Normal zone: 10.pages reserved 0.000000] Normal zone: 10.204768 u32768 u32768 u32768 u32768 u32768 0.0000000 pepu-alloc: of 0 d1.2768 u32768 u32768 u32768 u32768 0.0000000 pepu-alloc: 0 d1.0168: 256 (order - 1.132800n8 rinit=risbin/nit mem=64M [pi=744448 0.0000000 Dentry cache hash table entities: 369 (order - 3.32768 bytes) 0.0000000 Dentry cache hash table entities: 369 (order - 3.32768 bytes) 0.0000000 Dentry cache hash table entities: 369 (order - 3.32768 bytes) 0.0000000 Under-cache hash table entities: 369 (order - 3.32768 bytes) 0.0000000 Under-cache hash table entities: 369 (order - 4.1384 bytes) 0.0000000 Under-cache hash table entities: 369 (order - 4.1384 bytes) 0.0000000 Under-cache hash table entities: 4592 (order - 4.1384 bytes) 0.0000000 Under-cache hash table entities: 4592 (order - 4.1384 bytes) 0.0000000 Under-cache hash table entities: 4592 (order - 4.1384 bytes) 0.0000000 Under-cache hash table entities: 4592 (order - 4.1384 bytes) 0.0000000 Under-cache hash table entities: 4592 (order - 4.1484 bytes) 0.0000000 Under-cache hash table entities: 4592 (order - 4.1484 bytes) 0.0000000 Under-cache hash table entities: 550 (order - 1.1587 bytes) 0.0000000 Under-cache hash table entities: 550 (order - 1.1587 bytes) 0.0000000 Under-cache hash table entities: 550 (order - 1.1587 bytes) 0.0000001 Under-cache hash table entities: 550 (order - 1.3027 bytes) 0.000001	I	0.000000] CPU: VIVT data cache, VIVT instruction cache
0 000000 Nemory policy: Data cache virteback 0 0000000 Prearea_init_node: node 0 popdat c0657704, node_mem_map. c3777000 0 0000000 Normal Zone: 128 pages used on memmap. 0 0000000 Normal Zone: 128 pages used for memmap. 0 0000000 Normal Zone: 138 pages. LIFO batch: 3 0 0000000 Normal Zone: 1384 pages. LIFO batch: 3 0 0000000 popu-alloc: 100 0 0000000 Popu-alloc: 100 0 0000000 Popu-alloc: 100 0 0000000 PiD hash table entries: 256 (order: -2, 1024 bytes) 0 0000000 PiD hash table entries: 218 (order: 2, 1534 bytes) 0 0000000 Incide:S555K analiable (AS35K kernel code, 305K rvdata, 1704K rodata, 188K init, 252K bss, 7780K reserved, 0K cma-reserved) 0 0000000 Incide:S555K analiable (AS35K kernel code, 305K rvdata, 1704K rodata, 188K init, 252K bss, 7780K reserved, 0K cma-reserved) 0 0000000 Incide:S555K analiable (AS35K kernel code, 305K rvdata, 1704K rodata, 188K init, 252K bss, 7780K reserved, 0K cma-reserved) 0 0000000 Incide:S555K analiable (AS35K kernel code, 305K rvdata, 1704K rodata, 188K init, 252K bss, 7780K reserved, 0K cma-reserved) 0 0000000 Incide:S555K analiable (AS35K kernel code, 305K rvdata, 1704K rodata, 188K init, 252K bss, 7780K reserved, 0K cma-reserved) <t< td=""><td>I</td><td>0.000000] Machine model: Nuvoton NUC980 IOT-GateWay Version: 0.1</td></t<>	I	0.000000] Machine model: Nuvoton NUC980 IOT-GateWay Version: 0.1
0 000000 On node 0 totalpages: 16384 0 0000000 Normal zone: 0.2 gages reserved 0 0000001 Normal zone: 0.2 gages reserved 0 0000001 Normal zone: 0.3 gages reserved 0 0000001 Normal zone: 0.3 gages reserved 0 0000001 Normal zone: 0.3 gages reserved 0 0000001 pepu-altics: 0.0 di 32756 siloc=1'132766 0 0000001 pepu-altics: 0.0 di 32756 siloc=1'1320 (order: 2, 13246 bytes) 0 0000001 petuty cache hash table entrice: 3120 (order: 2, 15346 bytes) 0 0000001 petuty cache hash table entrice: 3120 (order: 2, 15346 bytes) 0 0000001 petuty cache hash table entrice: 3120 (order: 2, 15346 bytes) 0 0000001 petuty cache hash table entrice: 3120 (order: 2, 15346 bytes) 0 0000001 firmany: chroboloo - oxtfff00000 (44 KB) 0 0000001 firmany: chroboloo - oxtff00000 (44 KB) 0 0000001 firmany: chroboloo - oxtfff00000 (44 KB) 0	I	0.000000] Memory policy: Data cache writeback
0.000000 res_area_init_node: node 0, ppdd100857704, node_mem_map. C377000 0.000000 Normal Zone: 128 pages used or memmap. 0.000000 Normal Zone: 138 pages. LIFO batch: 3 0.000000 Normal Zone: 1384 pages. LIFO batch: 3 0.000000 popu-alloc: 10 0.0000001 Popu-alloc: 10 0.0000001 Popu-alloc: 10 0.0000001 Pip hash	I	0.000000] On node 0 totalpages: 16384
[0.00000] Normal zone: 128 pages used for memmap [0.000000] Normal zone: 16384 pages. LFO batch 3 [0.000000] Normal zone: 16384 pages. LFO batch 3 [0.000000] pcpu-allics: 07 01 d32768 u32768 alloc=1'33768 [0.000000] pcpu-allics: 261 d32768 u32768 alloc=1'33768 [0.000000] Kernel command line: root-idev/intdBloc42 console=tByS0, 115200n8 rdinit-/sbin/nit mem=64M [sp=744448 [0.000000] Diologood linead-coche hash table entries: 3192 (order: 2, 13276 bytes) [0.000000] Diologood linead-coche hash table entries: 4096 (order: 2, 13276 bytes) [0.000000] NickisSSSMR values (d3SSK kround code, 305K rividata, 1704K rodata, 188K init, 252K bas, 7780K reserved, 0K cma-reserved) [0.000000] NickisSSSMR values (d3SSK kround code, 305K rividata, 1704K rodata, 188K init, 252K bas, 7780K reserved, 0K cma-reserved) [0.000000] NickisSSSMR values (d3SSK kround code, 305K rividata, 1704K rodata, 188K init, 252K bas, 7780K reserved, 0K cma-reserved) [0.000000] NickisSSSMR values (d3SSK kround code, 305K rividata, 1704K rodata, 188K init, 252K bas, 7780K reserved, 0K cma-reserved) [0.000000] VickisSSMR values (d3SSK kround code, 305K rividata, 1704K rodata, 188K init, 252K bas, 7780K reserved, 0K cma-reserved) [0.000000] Ikits Notooloo - 0xc6000000 (4 MB)	ī	0.000000] free_area_init_node: node 0, pgdat c0657704, node_mem_map c3f77000
0.000000 Normal zone: 0 pages reserved 0.000000 pcpu-allice: s0 r0 d32768 alloc=1*32768 0.000000 pcpu-allice: 10 r0 0.000000 Built 1 zoneists in Zone order, mobility grouping on. Total pages: 16256 0.000000 Built 1 zoneists in Zone order, mobility grouping on. Total pages: 16256 0.000000 Built 1 zoneists in Zone order, mobility grouping on. Total pages: 16256 0.000000 Dentry cache hash table entries: 3519 (order: 3, 32768 bytes) 0.0000000 Memory layout 0.0000001 Memory layout	1	0.000000] Normal zone: 128 pages used for memmap
[0.00000] Normal zone: 16334 pages, LIPC batch: 3 [0.00000] popu-altics: 07 di 432768 u32768 alloc-1132768 [0.00000] Special command ine: not-id-wintdblock2 console-ttyS0, 115200n3 rdint=/sbin/init mem=64M [pi=744448. [0.00000] PID hash table entries: 256 (order -2, 1024 bytes) [0.00000] Dentry cache hash table entries: 3192 (order: 3, 32768 bytes) [0.00000] Index table entries: 3192 (order: 3, 32768 bytes) [0.00000] Index table entries: 3192 (order: 3, 32768 bytes) [0.000000] Index termel memory layout: [0.000000] Invalue: [0.0000000] Invalue:	I	0.000000] Normal zone: 0 pages reserved
[0.00000] pcpu-alicc: 90 d d32788 alloc-1132768 [0.00000] Built 1 zonelists in Zone order, mobility grouping on Total pages: 16256 [0.00000] PiD hash table entries: 256 (order: -2, 1024 bytes) [0.00000] PiD hash table entries: 3612 (order: 3, 32768 bytes) [0.00000] Memory: 57756K/65536K available (4538K kernel code, 305K rvdata, 1704K rodata, 188K init, 252K bss, 7780K reserved, 0K cma-reserved) [0.00000] Virtual kernel imemory layout [0.000000] Virtual kernel imemory layout [0.000000] Virtual kernel imemory layout [0.000000] vector: i.offf0000 - 0xfff1000 (44 kB) [0.000000] Virtual kernel imemory layout [0.000000] Virtual kernel imemory layout [0.000000] vector: i.offf0000 - 0xff800000 (3972 kB) [0.000000] vector: i.offf0000 - 0xff800000 (344 MB) [0.000000] imme: i.oxc4000000 - 0xc4000000 (164 MB) [0.000000] imme: i.oxc600000 - 0xc000000 - 0xc000000 - 0xc000000 - 0xc00000 - 0xc00000 - 0xc000000 - 0xc0000000 - 0xc000000 - 0xc000000 - 0xc000000 - 0xc000000 - 0x	1	0.000000] Normal zone: 16384 pages, LIFO batch:3
[0.00000] pcpu-alloc: [0] 0 0.000000] Kemel command line: root-/dev/mdblock2 console=ttyS0,115200n8 rdinit=/sbin/init mem=64M [pj=744448 0.000000] PLD hash table entries: 3256 (order: 2, 1024 bytes) 0.000000] Dentry cache hash table entries: 3025 (order: 3, 32768 bytes) 0.000000] Inode-cache hash table entries: 3025 (order: 3, 32768 bytes) 0.000000] Imode-cache hash table entries: 3025 (order: 2, 1634 bytes) 0.000000] Wemory: 5775K/65558 (arvitable) (453K kemel code, 305K rvdata, 1704K rodata, 188K init, 252K bas, 7780K reserved, 0K cma-reserved) 0.000000] Wemory: 5775K/65558 (arvitable) (453K kemel code, 305K rvdata, 1704K rodata, 188K init, 252K bas, 7780K reserved, 0K cma-reserved) 0.000000] Wemory: 5775K/65558 (arvitable) (453K kemel code, 305K rvdata, 1704K rodata, 188K init, 252K bas, 7780K reserved, 0K cma-reserved) 0.000000] wetcor: cotff0000 - 0xtf00000 (44 MB) 0.000000] wetcor: cotff0000 - 0xtf000000 (44 MB) 0.000000] Isk: cotff00000 - 0xcd000000 (18 MB) 0.000000] isk: cotff00000 - 0xcd000000 (18 KB) 0.000000] isk: cotff00000 - 0xcd000000 (18 KB) 0.000000] data: cotff00000 - 0xcd000000 (18 KB) 0.000000] Usk: sotff00000 - 0xcd000000 (253 KB) 0.000000] Usk: sotff00000 - 0xcd000000 (253 KB) 0.000000] Usk: sotff00000 - 0xcd00000 (253 KB) 0.000000] Usk: sotff00000 - 0xcd00000 (253 KB) 0.000000] Usk: sotff00000 - 0xcd000000 (253 KB) 0.000000] Usk: sotff00000 - 0xcd00000 (253 KB) 0.000000] Siss - 0xcd056784 (253 KB) 0.000000] Siss - 0xcd0567784 (254 KB) 0.000000] Siss - 0xcd05784 (254 KB) 0.000000] Siss - 0xcd05784 (254 KB) 0.000000] Siss - 0xcd05784 (254 KB) 0.000000] Siss - 0	I	0.000000] pcpu-alloc: s0 r0 d32768 u32768 alloc=1*32768
0 000000 Built 1 conelists in Zone order, mobility grouping on. Total pages: 18256 0 000000 Pice mel command line: root-/dev/mdblock/2 console=ttyS0,115200n8 rdinit-/sbin/init mem=64M lpj=744448 0 000000 Pice hash table entries: 285 (order: -2, 1024 bytes) 0 000000 Pice hash table entries: 305 (order: -2, 1024 bytes) 0 000000 Vitral kernel memory layout: 0 0000000 Itrana is 0.x0000000 0x44 MB) 0 0000001 I	ī	0.000000] pcpu-alloc: [0] 0
[0.00000] Kernel command line: root-idev/mtdblock2 console=thys0,115200n8 rdinit=/sbin/init mem=64M [pj=744448 [0.00000] DDh bash table entries: \$192 (order: 3, 32768 bytes) [0.00000] Memory: 57756/655548 (worder: 2, 1034 bytes) [0.00000] Wemory: 57756/655548 (worder: 4, 1024 bytes) [0.000000] Virtual kernel memory layout: [0.000000] fixmap: 0xffc0000 - 0xff10000 (4 kB) [0.000000] fixmap: 0xffc0000 - 0xff10000 (4 kB) [0.000000] module: 0xc0000000 - 0xf000000 (64 kB) [0.000000] module: 0xc0000000 - 0xc0000000 (16 kB) [0.000000] module: 0xc0000000 - 0xc0000000 (16 kB) [0.000000] int: 0xc0000000 - 0xc0000000 (16 kB) [0.000000] int: 0xc0000000 - 0xc00620754 (66 kB) [0.000000] iss: 0xc0662704 - 0xc0650878 (253 kB) [0.000000] reserptible hierarchical RCU implementation. [0.000000] NPL_BR25:45 [0.0000000] NPL_BR25:45 [1	0.000000] Built 1 zonelists in Zone order, mobility grouping on. Total pages: 16256
0.000000 PID hash table entries: 256 (order: 2, 1024 bytes) 0.0000000 Inder-cache hash table entries: 8192 (order: 3, 32768 bytes) 0.0000000 Inder-cache hash table entries: 4096 (order: 2, 1034 bytes) 0.0000000 Yitral kernel memory layout: 0.0000001 Indiverse Xitranov 0.0000001 Indit kerobelotter 0.000	ī	0.000000] Kernel command line: root=/dev/mtdblock2 console=ttyS0.115200n8 rdinit=/sbin/init mem=64M lpj=744448
1 0.000000 Dentry cache hash table entries: 3192 (order: 3.32786 bytes) 0.000000 Memory: 57756K/65536K available (4538K kernel code, 305K rvdata, 1704K rodata, 188K init, 252K bss, 7780K reserved, 0K cma-reserved) 0.000000 Virtual Kernel memory layout: 0.000000 Virtual Kernel memory layout: 0.000000 vector : controlloop - 0xff00000 (4 kB) 0.000000 vector : controlloop - 0xff00000 (54 kB) 0.000000 module : 0xc0000000 - 0xc40000000 (54 kB) 0.0000001 init : 0xc0051000 - 0xc06820F4 (264 kB) 0.0000001 init : 0xc0051000 - 0xc06820F4 (306 kB) 0.0000001 init : 0xc0651000 - 0xc06820F4 (306 kB) 0.0000001 init : 0xc0651081 (253 kB) 0.0000001 init : 0xc0651081 (253 kB) 0.0000001 init : 0xc0651081 (253 kB)	T	0.000000) PID hash table entries: 256 (order: -2, 1024 bytes)
[0.00000] Inode-cache hash table entries: 4096 (order: 2, 16384 by/se) [0.00000] Virtual kernel memory layout: [0.00000] Virtual kernel memory layout: [0.00000] Virtual kernel memory layout: [0.000000] Issue: 0xd4000000 - 0xc0620000 (16 MB) [0.000000] Issue: 1x 0xc0000000 - 0xc0680000 (18 KB) [0.000000] data - 0xc0680740 - 0xc0680000 (18 KB) [0.000000] SLUE: HVMalign=32. Order=0-3. MinObjects=0. CPUs=1. Nodes=1 [0.000000] SLUE: HVMalign=32. Order=0-3. MinObjects=0. CPUs=1. Nodes=1 [0.000000] Suid-time adjustment of leaf fanout to 32. [0.000000] Suid-time adjustment of leaf fanout to 32. [0.000000] Suid-time adjustment of leaf fanout to 32.	ī	0.000000] Dentry cache hash table entries: 8192 (order: 3, 32768 bytes)
0 000000 Memory: 57756K/65536K available (d538K kernel code, 305K rvdata, 1704K rodata, 188K init, 252K bas, 7760K reserved, 0K cma-reserved) 0 000000 Vector: 0.dff0000 - 0.mff0000 (4 kB) 0 000000 Vector: 0.dff0000 - 0.mff00000 (3072 kB) 0 000000 Vector: 0.dff0000 - 0.mff00000 (44 MB) 0 000000 Imme: 0.xcd000000 - 0.xcd000000 (64 MB) 0 000000 Imme: 0.xcd000000 - 0.xcd000000 (16 MB) 0 000000 Imme: 0.xcd000000 - 0.xcd000000 (16 MB) 0 000000 Imme: 0.xcd000000 - 0.xcd0020054 (6244 kB) 0 000000 Imme: 0.xcd000000 - 0.xcd092054 (306 kB) 0 000000 Imme: 0.xcd050000 - 0.xcd092054 (306 kB) 0 000000 Imme: 0.xcd050000 - 0.xcd092054 (305 kB) 0 000000 SLUB: HVNigne32, Order-0.x, MmObjects-0, CPUs=1, Nodes=1 0 000000 SLUB: HVNigne32, Order-0.x, MmObjects-0, CPUs=1, Nodes=1 0 000000 NPL_RC35.45 0 000001 NLB 0 000001 NLB 0 000001 NLB 0 000001 NPL_RC35.45 0 00001 NLB 0	ī	0.000000] Inode-cache hash table entries: 4096 (order: 2, 16384 bytes)
[0.00000] Virtual kernel memory layout: [0.000000] vector : ouffm0000 - 0xfff00000 (4 kB) [0.000000] fxmma p: 0xffc00000 - 0xfff00000 (944 MB) [0.000000] lowmen : 0xc0000000 - 0xc0000000 (64 MB) [0.000000] modules : 0xff000000 - 0xc0000000 (16 MB) [0.000000] itst: 0xc000000 - 0xc0000000 (18 MB) [0.000000] itst: 0xc000000 - 0xc0000000 (18 MB) [0.000000] data : 0xc060000 - 0xc0000000 (18 MB) [0.000000] data : 0xc060000 - 0xc0000000 (18 MB) [0.000000] data : 0xc060000 - 0xc0600000 (18 MB) [0.000000] bas : 0xc0600764 (306 KB) [0.000000] bas : 0xc060764 - 0xc0600874 (306 KB) [0.000000] SLUB: HV/align-32. Order=0. AJMnObjects-0. CPUs=1, Nodes=1 [0.000000] NE_HRA2:545 [0.000000] NE_HRA2:545 [0.000000] NE_HRA2:545 [0.000000] soled_cource: nuc980-limer5: mask: 0xfffff max_ycles: 0xfffff, max_idle_ns: 62215505635 ns [0.0000741] Console: colour dummy device 80x30 [0.186616[0:colour dummy device 80x30 [0.23133] Mount-cache hash table entries: 1024 (order: 0.4096 bytes) [0.23133] Mount-cache hash table entries: 1024 (order: 0.4096 bytes) [0.23133] Mount-cache hash table entries: 1024 (order: 0.4096 bytes) [0.23133] Mount-cache hash table entries: 1024 (order: 0.4096 bytes) [0.23133] Mount-cache hash table entries: 1024 (order: 0.4096 bytes) [0.23133] Mount-cache hash table entries: 1024 (order: 0.4096 bytes) [0.23134] Mount-cache hash table entries: 1024 (order: 0.4096 bytes) [0.231353] Mount-cache hash table entries: 1024 (order: 0.4096 bytes) [0.231353] Mount-cache hash table entries: 1024 (order: 0.4096 bytes) [0.23145] Mount-cache hash table entries: 1024 (orde	T	0.000000] Memory: 57756K/65536K available (4538K kernel code, 305K rwdata, 1704K rodata, 188K init, 252K bss, 7780K reserved, 0K cma-reserved)
[0.00000] vector : 0xfff000_chfff1000_(4 HE) [0.000000] formap : 0xff60000 chfff00000 (3072 HE) [0.000000] vmalice : 0x-4800000 - 0xff800000 (6 HME) [0.000000] iowrm : 0xc6800000 - 0xc6200500 (6 HME) [0.000000] iowrm : 0xc6800000 - 0xc6820f54 (6244 HE) [0.000000] i.ext : 0xc0850000 - 0xc682764 (306 HE) [0.000000] i.ext : 0xc0850000 - 0xc682764 (306 HE) [0.000000] i.ext : 0xc0850000 - 0xc682764 (306 HE) [0.000000] JLB: HY38ign=32, Order=0-3, MinObjects=0, CPUs=1, Nodes=1 [0.000000] SLUB: HY38ign=32, Order=0-3, MinObjects=0, CPUS=1, Nodes=1 [0.000001; SLUB: HY38ign=32, Order=0-3, MinObjects=0, CPUS=1, Nodes=1 [0.000001; SLUB: HY38ign=32, Order=0, 4098; SUS=0, SUS=0	ŕ	0.000000) Virtual kernel memory layout:
0 000000 fttmmap: 0xftc00000 - 0xft00000 (3072 kB) 0 000000 invalue: 0xc0000000 - 0xc0000000 (64 MB) 0 000000 invalue: 0xc0000000 - 0xc0000000 (18 MB) 0 000000 intel 0xc000000 - 0xc0000000 (18 KB) 0 000000 intel 0xc000000 - 0xc0000000 (18 KB) 0 000000 intel 0xc00000 - 0xc0000000 (18 KB) 0 000000 intel 0xc00000 - 0xc0000000 (25 KB) 0 000000 intel 0xc00000 - 0xc0000000 (25 KB) 0 000000 intel 0xc00000 - 0xc0000000 (18 KB) 0 000000 intel 0xc00000 - 0xc0000000 (25 KB) 0 000000 intel 0xc000000 - 0xc0000000 (25 KB) 0 0000000 Intel 0xc000000 (25 KB) 0 0000000 Preemptible hierarchical RCU implementation. 0 0000000 NR-IRQS 545 0 00000001 NLRAS 545 0 0000001 Intel 0xe10 KB 0 0000001 Intel xe10 KB 0 0000001 Intel xe10 KB 0 0000001 Intel xe10 KB <td>i</td> <td>0.000000] vector : 0xffff0000 - 0xffff1000 (4 kB)</td>	i	0.000000] vector : 0xffff0000 - 0xffff1000 (4 kB)
[0.00000] vmalle: :0xc4800000 - 0xt800000 (944 MB) (000000) [0.000000] lowmem : 0xc0000000 - 0xc0800000 (18 MB) (000000) [0.000000] itext: 0xc0008000 - 0xc0820f54 (2644 kB) (000000) [0.000000] itext: 0xc0068000 - 0xc0680764 (2644 kB) (0000000) [0.000000] itext: 0xc0650000 - 0xc0680764 (306 kB) (000000) [0.000000] bas: 0xc0650000 (18 kB) (000000) [0.000000] bas: 0xc0650000 (18 kB) (000000) [0.000000] bas: 0xc0650000 (18 kB) (000001) [0.000000] build-time adjustment of leaf fancut to 32. [0.000000] build-time adjustment of leaf fancut to 32. [0.000000] colscale (000001) [0.000001] colscale (00001) [0.000001] build-time adjustment of leaf fancut to 32. [0.000001] colscale (00001) [0.000001] build-time adjustmen	ī	0.000000] fixmap : 0xffc00000 - 0xfff00000 (3072 kB)
[0.00000] lowmern : 0xc0000000 - 0xc4000000 (f4 MB) [0.000000] itext : 0xc0000000 - 0xc0000000 (f6 MB) [0.000000] itext : 0xc0000000 - 0xc0000000 (f8 KB) [0.000000] itext : 0xc000000 - 0xc000000 (f8 KB) [0.000000] itext : 0xc000000 - 0xc000000 (f8 KB) [0.000000] itext : 0xc000000 - 0xc000000 (f8 KB) [0.000000] itext : 0xc000000 - 0xc000000 (f8 KB) [0.000000] itext : 0xc000000 - 0xc000000 (f8 KB) [0.000000] itext : 0xc000000 - 0xc0000000 (f8 KB) [0.000000] itext : 0xc000000 - 0xc0000000 (f8 KB) [0.000000] itext : 0xc000000 - 0xc0000000 (f8 KB) [0.000000] itext : 0xc000000 - 0xc0000000 (f8 KB) [0.000000] piest : 0xc0000000 - 0xc00000000000000000000000	ŕ	0.0000001 vmalloc: 0xc4800000 - 0xff800000 (944 MB)
[0.00000] modules : 0xbt00000 - 0xc0600000 (16 MB) [0.000000] i.ext : 0xc008000 - 0xc065000 (18 MB) [0.000000] i.ext : 0xc008100 - 0xc065000 (18 MB) [0.000000] i.ext : 0xc0651000 - 0xc0650764 (306 KB) [0.000000] b.ss : 0xc065000 - 0xc065074 (306 KB) [0.000000] b.ss : 0xc0651080 - 0xc0650764 (306 KB) [0.000000] b.ss : 0xc065000 + 0xc0650764 (306 KB) [0.000000] b.ss : 0xc065000 + 0xc0650764 (306 KB) [0.000000] b.ss : 0xc065000 + 0xc0650764 (306 KB) [0.000000] b.ss : 0xc065000 + 0xc0650764 (306 KB) [0.000000] b.ss : 0xc0605080 + 0xc0650764 (306 KB) [0.000000] b.ss : 0xc0605080 + 0xc0650764 (306 KB) [0.000000] Build-time adjustment of leaf fanout to 32. [0.000000] b.ss : 0xc0001 mmg/max_cycles: 0xfffff max_cycles: 0xfffff, max_cide_ns: 62215505635 ns [0.000001] b.ss : 0xc0001 mmg/max_cycles: 0xfffff max_cycles: 0xfffff, max_cide_ns: 62215505635 ns [0.000001] class : 10216041 max_cycles: 0xfffff max_cycles: 0xfffff [0.000001] class : 10216041 max_cycles: 0xfffff max_cycles: 0xfffff [0.00001] class : 10216041 max_cycles: 0xfffff	ī	0.000000] lowmem : 0xc0000000 - 0xc4000000 (64 MB)
[0.00000] text: 0xc0008000 - 0xc06520754 (324 4E) [0.000000] init: 0xc06521000 - 0xc0650000 (188 kB) [0.000000] init: 0xc06521000 - 0xc0650000 (253 kB) [0.000000] isi: 0xc0650784 - 0xc0650080 (253 kB) [0.000000] isi: 0xc0652100 - 0xc0650780 (253 kB) [0.000000] isi: 0xc0651084 - 0xc0650808 (253 kB) [0.000000] StuB: HVMigins-32, Order=0.0, CPUS=1, Nodes=1 [0.000000] StuB: HVMigins-32, Order=0.0, CPUS=1, Nodes=1 [0.000000] Build-time adjustment of leaf fanout to 32. [0.000000] Colored studies [0.000000] Colored studies [0.000000] NR_JRC2S-545 [0.000001] Colored studies [0.000003] sched_clock: 24 bits at 120kHz, resolution 8333ns, viraps every 69905062489ns [0.00001] Colored times/020 (enabled [0.00001] Calibriang delay loop (skipped) preset value. 148.85 BogoMIPS (lpj=744448) [0.1980174] Calibriang delay loop (skipped) preset value. 148.85 BogoMIPS (lpj=744448) [0.1980174] Calibriang delay loop (skipped) preset value. 148.85 BogoMIPS (lpj=744448) [0.1980174] Calibriang delay loop (skipped) preset value. 148.85 BogoMIPS (ŕ	0.0000001 modules : 0xbf000000 - 0xc0000000 (16 MB)
[0 00000] init: 0xc0621000 - 0xc0650000 (188.KB) [0 000000] bas: 0xc06602784 - 0xc06db878 (253.KB) [0 000000] Build-time adjustment of leaf fanout to 32. [0 000000] Build-time adjustment of leaf fanout to 32. [0 000000] Clocksource: nuc980-timer5: mask: 0xffffff max_cycles: 0xfffff, max_idle_ns: 62215505635 ns [0 000000] Clocksource: nuc980-timer5: mask: 0xfffff max_cycles: 0xfffff, max_idle_ns: 62215505635 ns [0 000003] Scheductor dummy device 80x30 [0 000004] Clashorting delay loop (skipped) preset value. 148.88 BogoMIPS (lpj=744448) [0 198174] pid_max: default: 32768 minimum: 301 Clashorting delay loop (skipped) preset value. 148.88 BogoMIPS (lpj=744448) [0 203133] Mount-cache hash table entries: 1024 (order: 0, 4096 bytes) Clashorting delay loop (skipped) preset value. 148.88 BogoMIPS (lpj=744448) [0 229433] Mountpoint-cache hash table entries: 1024 (order: 0, 4096 bytes) Clashorting delay loop (skipped) preset value. 148.88 BogoMIPS (lpi=744448) [0 229433] Mountpoint-cache hash table entries: 1024 (order: 0, 4096 bytes) Clas3163 (lpi + shash table entrintes: 1024 (order: 0, 409	r	0.0000001 text: 0xc0008000 - 0xc0620/54 (6244 kB)
[0.00000] dda: 0xc055000-0xc0596784 (306 kB) [0.000000] bbs: 0xc0659784 - 0xc06b0784 (253 kB) [0.000000] SUB: HVMigna32, Order-0-3, MinObjects=0, CPUs=1, Nodes=1 [0.000000] SUB: HVMigna32, Order-0-3, MinObjects=0, CPUs=1, Nodes=1 [0.000000] Build-time adjustment of leaf fanout to 32. [0.000000] NE, IRX625-845 [0.000000] NE, IRX625-845 [0.000003] Sched, Lock: 24 bits at 120kHz, resolution 8333ns, wraps every 69905062489ns [0.000003] Console: colour dummy device 80x30 [0.168616] console (thy50) enabled [0.168616] console (thy50) enabled [0.190091] Calitating delay loop (skipped) preset value. 148.88 BogoMIPS (lpj=744448) [0.19017] Calitating delay loop (skipped) preset value. 148.88 BogoMIPS (lpj=744448) [0.203133] Mountpoint-cache hash table entrise: 1024 (order: 0, 4096 bytes) [0.203133] Mountpoint-cache hash table entrise: 1024 (order: 0, 4096 bytes) [0.2218916] CPU: Testing write buffer coherency: ok [0.2218916] CPU: Testing write buffer coherency: ok [0.2218916] CPU: Testing write buffer ox0: SMITHT max_cycles: 0xfITHT, max_idle_ns: 19112604462750000 ns	r	0.0000001 init: 0xc0621000 - 0xc0650000 (188 kB)
[0 00000] bss: 0xc069c784 - 0xc06uc88 (253 kB) [0 000000] SUBJEHWailgn32, Order-0.3, Min Objects=0, CPUs=1, Nodes=1 [0 000000] Preemptible hierarchical RCU implementation. [0 000000] Buld-time adjustment of leaf fanout to 32. [0 000000] Cocksource: nuc880-timer5: mask: 0xfffff max_cycles: 0xfffff, max_idle_ns: 62215505635 ns [0 000000] clocksource: nuc880-timer5: mask: 0xfffff max_cycles: 0xfffff, max_idle_ns: 62215505635 ns [0 000000] Clocksource: nuc880-timer5: mask: 0xfffff max_cycles: 0xfffff, max_idle_ns: 62215505635 ns [0 000001] Clocksource: nuc880-timer5: mask: 0xfffff max_cycles: 0xfffff, max_idle_ns: 62215505635 ns [0 000003] clocksource: nuc880-timer5: mask: 0xfffff max_cycles: 0xfffff, max_idle_ns: 62215505635 ns [0 000003] Clocksource: nuc880-timer5: mask: 0xfffff max_cycles: 0xfffff, max_idle_ns: 62215505635 ns [0 00003] clocksource: nuc880-timer5: mask: 0xfffff max_cycles: 0xfffff, max_idle_ns: 62215505635 ns [0 000003] Clocksource: nuc880-timer5: no24 (order: 0, 4096 bytes) [0 190051] Calibrating delay loop (skipped) preset value.: 148.88 BogoMIPS (lpi=744448) [0 190051] Sottimute buffer coherency: ok [0 220453] Sottimute buffer coherency: ok [0 220453] Sottimute buffer coherency: ok [0 220453] Clocksource: inflait_cled priot fors: 10024 (order: 0, 4096 bytes) [0 220453] Sottic dentitis may for 0x8400 - 0x8430	ŕ	0.0000001 data : 0xc0650000 - 0xc069c784 (306 kB)
0 000000 SLUB: HWalign-32, Order-0.3, MinObjects=0, CPUs=1, Nodes=1 0 000000 Build-time adjustment of leaf fanult 53. 0 0000000 Build-time adjustment of leaf fanult 53. 0 000001 Schedult 24 bits at 120kHz, resolution 8333ns, wraps every 69905062489ns 0 0000741 Console: clock: 24 bits at 120kHz, resolution 8333ns, wraps every 69905062489ns 0 0000741 Console: clock: 24 bits at 120kHz, resolution 8333ns, wraps every 69905062489ns 0 0000741 Console: clock: 24 bits at 120kHz, resolution 8333ns, wraps every 69905062489ns 0 0000741 Console: clock: 24 bits at 120kHz, resolution 8333ns, wraps every 69905062489ns 0 000741 Console: clock: 24 bits at 120kHz, resolution 8333ns, wraps every 69905062489ns 0 186174 Console: clock: 24 bits at 120kHz, resolution 8333ns, wraps every 69905062489ns 0 186174 Console: clock: 24 bits at 120kHz, resolution 8333ns, wraps every 69905062489ns 0 186174 Console: clock: advery a	r	0.0000001 bss: 0xc069c784 - 0xc06db8f8 (253 kB)
0 000000 Preemptible herarchical RCU implementation. 0 000000 Build-time adjustment of leaf fanout to 32. 0 000000 NR_IRQS:545 0 000000 Icks and the structure adjustment of leaf fanout to 32. 0 000000 Icks and the structure adjustment of leaf fanout to 32. 0 0000000 Icks and the structure adjustment of leaf fanout to 32. 0 0000000 Icks and the structure adjustment of leaf fanout to 32. 0 0000000 Icks and the structure adjustment of leaf fanout to 32. 0 0000000 Icks and the structure adjustment of leaf fanout to 32. 0 0000000 Icks and the structure adjustment of leaf fanout to 32. 0 0000000 Icks and the structure adjustment of leaf fanout to 32. 0 1886160 Icosside (ttys)00 0 1886160 Icosside (ttys)00 0 1981741 Judication adjustment adju	ř	0.000000] SLUB: HWalign=32. Order=0-3. MinObjects=0. CPUs=1. Nodes=1
[0 000000] Build-time adjustment of leaf fanout to 32. [0 000000] NR_IRQS:545 0 0000031 sched_clock:source:nuc980-timer5: mask. 0xffffff max_cycles: 0xfffff, max_idle_ns: 62215505635 ns [0 000003] sched_clock: 24 bits at 120kHz, resolution 8333ns, wraps every 69905062489ns 0 000741 Console: colour dummy device 80x30 [0 000001 Console: colour dummy device 80x30 0 1806161 Console: colour dummy device 80x30 [0 1806174] pid_max: default. 32768 minimum: 301 0 19801741 pid_max: default. 32768 minimum: 301 [0 203133] Mount-cache hash table entries: 1024 (order: 0, 4096 bytes) 0 203133 Mount-cache hash table entries: 1024 (order: 0, 4096 bytes) [0 229763] Setting up static identify map or 0x8400 - 0x843c 0 22715581 clocksource: jiffles: mask: 0xfffffff max_cycles: 0xfffffff, max_idle_ns: 19112604462750000 ns [0 228453] NET: Registered protocol family 16 0 20895831 NET. Registered protocol family 16 [0 239453] NET: Registered protocol family 16 0 3031999 DMA: prealicedate 256 KLB pool for atomic coherent allocations	ŕ	0.0000001 Preemptible hierarchical RCU implementation.
0 000000j NR_JROS:545 0 000000j lockisource: nuce80-limer5: mask: 0xfffff max_cycles: 0xfffff, max_idle_ns: 62215505635 ns 0 000003j sched_clock: 24 bits at 120kHz, resolution 8333ns, vraps every 69905062489ns 0 0000741 Console (th;50) enabled 0 186616 console: colour dummy device 80x30 0 186616 console: colour dummy device 80x30 0 186616 console (th;50) enabled 0 186617 jud_max: cdeatul: 32768 minimum: 301 0 189174 jud_max: cdeatul: 32768 minimum: 301 0 203133 Mount-cache hash table entries: 1024 (order: 0, 4096 bytes) 0 2030130 Mountpoint-cache hash table entries: 1024 (order: 0, 4096 bytes) 0 2031618 Mountpoint-cache hash table entries: 1024 (order: 0, 4096 bytes) 0 202180163 CPU: Testing write buffer coherency: ok 0 202180163 CPU: Testing write buffer coherency: ok 0 202180163 Index, hash table entries: 1256 (order: -1, 3072 bytes) 0 2283163 futex hash table entries: 1256 (order: -1, 3072 bytes) 0 228433 NET. Registered protocol family 16 0 208433 NET. Registered protocol family 16 0 3031999 DMA: preallocated 256 KIB pool for atomic coherent allocations	ī	0.000000] Build-time adjustment of leaf fanout to 32.
O 000000 clocksource: nuc880-limen5: mask: 0xfffff max_cycles: 0xfffff, max_idle_ns: 62215505635 ns O 000033 sched_clock: 24 bits at 120kHz, resolution 8333ns, wraps every 69905062489ns O 000741 Console: colour dummy device 80x30 O 188616] console (thy50] enabled O 188616] console (thy50] enabled O 190091 [Calibrating delay loop (skipped) preset value. 148.88 BogoMIPS (tpj=744448) O 190091 [Calibrating delay loop (skipped) preset value. 148.88 BogoMIPS (tpj=744448) O 190091 [Calibrating delay loop (skipped) preset value. 148.88 BogoMIPS (tpj=744448) O 190091 [Calibrating delay loop (skipped) preset value. 148.88 BogoMIPS (tpj=744448) O 190091 [Calibrating delay loop (skipped) preset value. 149.88 BogoMIPS (tpj=744448) O 2019078] Mount-cache hash table entries: 1024 (order: 0, 4096 bytes) O 219708[Mount-cache hash table entries: 1024 (order: 0, 4096 bytes) O 219708[Mount-cache hash table entries: 1024 (order: 0, 4096 bytes) O 219708[Mount-cache hash table entries: 1024 (order: 0, 4096 bytes) O 2219516] CPU. Testing write buffer coherency: ok O 22971558] clocksource: jiffies: mask: 0xfffffff, max_cycles: 0xffffff, max_idle_ns: 19112604462750000 ns O 225433] NET. Registered protocol family 16 O 205433] NET. Registered protocol family 16 O 303199D JMA: preallocated 256 KiB pool for atomic coherent allocations	ŕ	0.000001 NR IRQS.545
Co000333 sched_clock: 24 bits al 120kHz_resolution 8333ns, wraps every 69905062489ns Co000333 sched_clock: 24 bits al 120kHz_resolution 8333ns, wraps every 69905062489ns Console: [th/50] enabled Console [th/50	ī	0.000000] clocksource: nuc980-timer5: mask: 0xffffff max cycles: 0xffffff, max idle ns: 62215505635 ns
0 000741] Console: colour dummy device 80x30 0 168616] console (thy50] enabled 0 190091] Calibrating delay loop (skipped) preset value 148.85 BogoMIPS (tpj=744448) 0 190091] Calibrating delay loop (skipped) preset value 148.85 BogoMIPS (tpj=744448) 0 191091] Calibrating delay loop (skipped) preset value 148.85 BogoMIPS (tpj=744448) 0 191091] Calibrating delay loop (skipped) preset value 148.85 BogoMIPS (tpj=744448) 0 2031331 Mount-cache hash table entries: 1024 (order: 0, 4096 bytes) 0 209708] Mountpoint-cache hash table entries: 1024 (order: 0, 4096 bytes) 0 209708] Mountpoint-cache hash table entries: 1024 (order: 0, 4096 bytes) 0 209708] Mountpoint-cache hash table entries: 1024 (order: 0, 4096 bytes) 0 209708] Mountpoint-cache hash table entries: 1024 (order: 0, 4096 bytes) 0 209708] Mountpoint-cache hash table entries: 1024 (order: 0, 4096 bytes) 0 2249738] Setting up static identity map for 0x8400 - 0x8436 0 2289738] clocksource: jiffies: mask: 0xfffffff, max_cycles: 0xfffffff, max_ilde_ns: 19112604462750000 ns 0 228974] pincht core: initialized pinctif subsystem 0 228974] pincht core: initialized pinctif subsystem 0 2080741 pincht core: initialized pinctif subsystem 0 2080741 pinctif core and 256 KiB col for atomic coherent allocations	ŕ	0.000033] sched clock: 24 bits at 120kHz, resolution 8333ns, wraps every 69905062489ns
1.186115j console (ttyS0j enabled 1.186615j console (ttyS0j enabled 1.90091] Calibrating delay loop (skipped) preset value 148.88 BogoMIPS (tpj=744448) 1.90091] Calibrating delay loop (skipped) preset value 148.88 BogoMIPS (tpj=744448) 1.90174 pid_max. default. 32768 minimum: 301 0.203133 Mount-cache hash table entries. 1024 (order: 0, 4096 bytes) 0.203133 Mountpoint-cache hash table entries. 1024 (order: 0, 4096 bytes) 0.218916] CPU. Testing write buffer coherency: ok 0.2218916] Setting up static identify map for 0x8400 - 0x843c 0.2213158] clocksource: jiffles: mask: 0xfffffff max_cycles: 0xfffffff, max_idle_ns: 19112604462750000 ns 0.288674] pincht core: initialized pincht subsystem 0.298433] NET. Registered protocol family 16 0.303199] DMA: preallocated 256 KiB pool for atomic coherent allocations	ŕ	0.0007411 Console: colour dummy device 80x30
0.190091] Calibrating delay loop (skipped) preset value148.88 BogoMIPS (lpj=744448) 0.1980741 pid_max: default.32788 minimum: 301 0.2031331 Mount-cache hash table entries: 1024 (order: 0, 4096 bytes) 0.203708] Mountpoint-cache hash table entries: 1024 (order: 0, 4096 bytes) 0.218716] CPU: Testing write buffer coherency: ok 0.2271558] clocksource: jiffies: mask: 0xfffffff max_cycles: 0xfffffff, max_idle_ns: 19112604462750000 ns 0.288874] pincht core: initialized pincht subsystem 0.284833] NET: Registered protocol family 16 0.303199] DMA: preallocated 256 KIB pool for atomic coherent allocations	ī	0.186616] console [tty/S0] enabled
[0.198774] pid_max: default: 32768 minimum: 301 [0.203708] Mountpoint-cache hash table entries: 1024 (order: 0, 4096 bytes) [0.209708] Mountpoint-cache hash table entries: 1024 (order: 0, 4096 bytes) [0.218916] CPU: Testing write buffer coherency: ok [0.224983] Setting up static identify map for 0x8400 - 0x843c [0.2271558] clocksource: jiffies: mask: 0xfffffff max_cycles: 0xfffffff, max_idle_ns: 19112604462750000 ns [0.22843316] futex hash table entries: 256 (order: -1, 3072 bytes) [0.286433] NET: Registered protocol family 16 [0.3903199] DMA: preallocated 256 KiB pool for atomic coherent allocations	ŕ	0.1900911 Calibrating delay loop (skipped) preset value148.88 BogoMIPS (lpi=744448)
0 203133] Mount-cache hash table entries: 1024 (order: 0, 4096 bytes) 0 209706] Mountpoint-cache hash table entries: 1024 (order: 0, 4096 bytes) 0 218916] CPU: Testing write buffer coherency: ok 0 224983] Setting up static identity map for 0x8400 - 0x843c 0 2271558] clocksource: jiffies: mask: 0xfffffff max_cycles: 0xfffffff, max_idle_ns: 19112604462750000 ns 0 2271558] clocksource: jiffies: mask: 0xfffffff max_cycles: 0xfffffff, max_idle_ns: 19112604462750000 ns 0 282316] futex hash table entries: 256 (order: -1, 3072 bytes) 0 286433] NET: Registered protocol family 16 0 303199] DMA: preallocated 256 KIB pool for atomic coherent allocations	r	0.198174) pid max: default: 32768 minimum: 301
[0 209708] Mountpoint-cache hash table entries: 1024 (order: 0, 4096 bytes) [0 218916] CPU: Testing write buffer coherency: ok [0 2214935] Setting up static identity map for 0x8400 - 0x843c [0 2271558] clocksource: jiffies: mask: 0xfffffff max_cycles: 0xfffffff, max_idle_ns: 19112604462750000 ns [0 282674] pincht core: initialized pinctif subsystem [0 286433] NET: Registered protocol family 16 [0 303199] DMA: preallocated 256 Kif Bool for atomic coherent allocations	ř	0.2031331 Mount-cache hash table entries: 1024 (order: 0.4096 bytes)
1 0.218916] CPU: Testing write buffer coherency: ok 2218916] CPU: Testing write buffer coherency: ok 0.221856] clocksource jiffies: mask: 0x8400 - 0x8430 0.271556] clocksource jiffies: mask: 0x0ffmff max_cycles: 0xffmff, max_idle_ns: 19112604462750000 ns 0.282316] futex hash table entries: 256 (order: -1, 3072 bytes) 0.288674] pincht core: initialized pincht aubsystem 0.298433] NET: Registered protocol family 16 0.393199] DMA: preallocated 256 KiB pool for atomic coherent allocations	ŕ	0.2097081 Mountpoint-cache hash table entries: 1024 (order: 0. 4096 bytes)
[0.224983] Setting up static identity map for 0x8400 - 0x843c [0.271558] clocksource: jiffles: mask: 0xfffffff max_cycles: 0xfffffff, max_cldle_ns: 19112604462750000 ns [0.282316] fuex hash table entries: 256 (order: -1.3072 bytes) [0.28874] pinctrl core: initialized pinctrl subsystem [0.286433] NET: Registered protocol family 16 [0.3903199] DMA: preallocated 256 KIB pool for atomic coherent allocations	r	0.218916) CPU: Testing write buffer coherency: ok
[0.271558] clocksource: jiffles: mask: 0xfffffff max_cycles: 0xfffffff, max_idle_ns: 19112604462750000 ns [0.282316] futex hash table entries: 256 (order: -1, 3072 bytes) [0.288674] princht core: initialized princht subsystem [0.288674] NET: Registered protocol family 16 [0.393199] DMA: preallocated 256 KIB pool for atomic coherent allocations	ř	0 224983) Setting up static identity map for 0x8400 - 0x843c
0.282316] futex hash table entries: 256 (order: -1, 3072 bytes) 0.288874] pinctri core: initialized pinctri subsystem 0.296433] NET: Registered protocol family 16 0.303199 DMA: preallocated 256 KIB pool for atomic coherent allocations	r	0.271558) clocksource: liffies: mask: 0xfffffff max cycles: 0xfffffff max idle ns: 19112604462750000 ns
0 288874] pinctrl core: initialized pinctrl subsystem 0 286433] NET: Registered protocol family 16 0 303199] DMA: preallocated 256 KIB pool for atomic coherent allocations	r	0.282316) futex hash table entries: 256 (order: -1.3072 bytes)
0.296433] NET: Registered protocol family 16 0.303199] DMA: preallocated 256 KiB pool for atomic coherent allocations	i	0.288874 pinctri core: initialized pinctri subsystem
0.303199] DMA: preallocated 256 KiB pool for atomic coherent allocations	r	0.2964331 NET: Registered protocol family 16
	ŕ	0.3031991 DMA: preallocated 256 KIB pool for atomic coherent allocations
[0.316783] <dt> nuc980 dt device init +</dt>	r	0.316783] <dt> nuc980 dt device init +</dt>

Serial Module -

5.7.2 System

5.7.2.1 System

BL200UA	Status -	System -	Settings -	I/O Module -	Serial Module 🕶	OPC UA -	Operation&Control -	Logout	REFRESHING
System Here you can confi System Prop	gure the ba: erties	System Administra Backup / I Firmware Reboot	ation Flash	ke its hostnam	e or the timezone.				
General Settings	Logging	anne soyne		canguage and \$	Style				
	Local Time	2022/3/ Sync	21 下午2:58:5 with browser	6 Sync with M	NTP-Server				
	Hostname	BL200U	JA						
	Timezone	UTC		~					
							Save & Apply	• Sav	Reset
			Sher	nzhen Beilai Tec	hnology Co.,Ltd (v1.	0.11) / 2022-0	2-17		

System Properties > General Settings

BL203 Ethernet/IP Distributed I/O Coupler



Item	Description	Default	
	Displays the current time of the device. You can		
Local time	click the "Sync browser time" or "Sync with NTP		
	server" button to update the device time.		
	The device name can be customized to easily		
Hostname	distinguish between multiple devices.	BL200EI	
Timezone	The time zone can be selected via the drop down		
	menu		

System Properties > Logging

BL200UA	Status -	System - Setti	ngs - I/O Module -	- Serial Module -	OPC UA *	Operation&Control -	Logout	REFRESHING
System Here you can config System Prope	gure the bas erties	sic aspects of your	device like its hostnar	ne or the timezone.				
General Settings	Logging	Time Synchroniza	tion Language and	Style				
System log	g buffer size	e 64 @ kiB						
External system	m log server	0.0.0.0						
External system log	server por	514						
External system	m log server protoco	UDP	¥					
Write syste	em log to file	/tmp/system.lo	g					
Log	output leve	Debug	~					
Cro	n Log Leve	Debug	~					
						Save & Apply	Save	Reset

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Item	Description	Default
System log buffer size		64
External system log server		
External system log server		
port		
External system log server		
protocol		
Write system log to file		
Log output level		
Cron log level		



System Properties > Time Synchronization

An NTP server can be set to synchronize time

BL200UA Status -	System - Settings -	I/O Module -	Serial Module -	OPC UA -	Operation&Control -	Logout	REFRESHING
System Here you can configure the basic	c aspects of your devic	e like its hostname	or the timezone.				
System Properties							
General Settings Logging	Time Synchronization	Language and S	tyle				
Enable NTP client							
Provide NTP server							
Use DHCP advertised servers							
NTP server candidates	0.openwrt.pool.ntp.	org					
	1.openwrt.pool.ntp.	org ×					
	2.openwrt.pool.ntp.	org ×					
	3.openwrt.pool.ntp.	org					
		+					
					Save & Apply	• Save	Reset

System Properties > Language and Style

BL200UA	Status -	System -	Settings -	I/O Module -	Serial Module +	OPC UA -	Operation&Control -	Logout	REFRESHING
System Here you can confi System Prop	gure the bas erties	sic aspects o	f your device	like its hostname	or the timezone.				
General Settings	Logging	Time Syncl	hronization	Language and S	tyle				
	Language	auto		~					
	Design	Bootstra	ар	¥					
							Save & Apply	Save	Reset

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Item	Description	Default
Language	Available in auto, English, Chinese	auto
Design	Currently only Bootstrap is supported.	Bootstrap

5.7.2.2 Administration

Administration > Router Password



Change the administrator password for accessing the device.

BL200UA	Status -	System -	Settings -	I/O Module -	Serial Module -	OPC UA -	Operation&Control -	Logout
Router Password	SSH-Key	System Administra	ation					
Router Pa Changes the admin	ISSWO nistrator pas Passwore	Backup / f Firmware Reboot	-lash	vice				
	Confirmation	n		8				
								Save

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Administration > SSH Keys

Public keys allow for the passwordless SSH logins with a higher security compared to the use of regular passwords. In order to upload a new key to the device, paste an OpenSSH compatible public key line or drag a .pub file into the input field.

BL200UA	Status -	System -	Settings -	I/O Module -	Serial Module -	OPC UA -	Operation&Control -	Logout
Router Password	SSH-Key	s						
SSH-Keys								
Public keys allow for an OpenSSH compa	r the passw atible public	ordless SSF key line or	l logins with a drag a	higher security file into the input	compared to the use field.	e of plain pass	words. In order to upload	I a new key to the device, paste
No public keys pres	ent yet.							
		Add	OV					

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5.7.2.3 Backup/Flash Firmware

BL200UA	Status 🔫	System -	Settings -	I/O Module -	Serial Module -	OPC UA -	Operation&Control -	Logout				
Flash op	eration	System Administ										
Actions Config	guration	Backup / Firmware	Flash									
Backup												
Click "Generate a	Click "Generate archive" to download a tar archive of the current configuration files.											
Download backup Generate archive												
Restore												
To restore configue possible with square	To restore configuration files, you can upload a previously generated backup archive here. To reset the firmware to its initial state, click "Perform reset" (only possible with squashfs images).											
Re	set to default	Perfo	orm reset									
R	estore backup	p Uplo	ad archive									
		Cus	tom files (ce <mark>r</mark> tif	ficates, scripts) n	nay remain on the s	ystem. To prev	ent this, perform a facto	ry-reset first.				
Save mtdbl	ock conte	ents										
Click "Save mtdb	lock" to down	load specifi	ed mtdblock file	e. (NOTE: THIS I	EATURE IS FOR P	ROFESSION	ALS!)					
Cho	oose mtdbloci	k u-boot		~								
Down	lioad mtdbloc	k Save	mtdblock									
Flash new f Upload a sysupgr	irmware i rade-compatil Image	i mage ble image he e Flasl	ere to replace ti n image	he running firmw	are.							
			Sher	nzhen Beilai Tech	nology Co.,Ltd (v1.	0.11) / 2022-02	2-17					
Item		[Descript	tion				Default				
Dealum		(Click "Ge	enerate a	archive" to	downloa	ad a tar					
Баскир		a	archive of the current configuration files.									
		٦	To restor	e configu	uration files	s, you ca	an upload a					
		r I	orevious	ly genera	ated backu	p archiv	e here. To					
Restore		r	reset the firmware to its initial state, click									
		"	"Perform reset" (only possible with squashfs									
		i	mages).									
		(Click "Sa	ave mtdb	lock" to do	wnload	specified					
Save mtdblcok		r	ntdblock	c file. (NC	DTE: THIS	FEATU	RE IS FOR					
		F	PROFESSIONALS)									

replace the running firmware.

Upload a sysupgrade-compatible image here to

--

Flash image



5.7.2.4 Reboot

Click "Perform reboot" will reboot your device



5.7.3 Settings

Device settings					
Device settings					
Modbus Device ID	1				
	If not set or set to 0, the device ID	in the Modbus comman	id is ignored		
Modbus TCP port	502				
Dial switch address	192.168.1.253 The 3rd segment of IP address is	determined by dial switc	ch, restart the	e device and the modific	cation will take effect
IP Address Type	Static Address 🗸				
Set device IP address					
Subnet Mask	255.255.255.0				
Gateway address					
				Save & Apply	• Save Reset

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Item	Description	Default	
Modbus Device ID	Modbus device ID range is 1~247.	1	
Madhua TCD part	Modbus TCP protocol port number, which can	502	
	be customized.		
DIP switch address	Displays the IP address set by the DIP switch.		
ID address type	Select from "Static Address", "Dynamic		
IF address type	Address(DHCP)".		
Set device IP	The IP address of the device can be set by		
address	yourself, and it needs to be restarted to take		



	effect after setting.	
Subnet mask	Set IP subnet mask	
Gateway address	Set IP gateway address	

5.7.4 I/O Modules

After power on, the controller automatically recognizes all I/O modules connected to it and creates an internal local process image based on the module type, data width and the module's position in the node.

If I/O modules are added, changed or removed, a new process image is created and the process data addresses change. When adding an I/O module, the process data of all previous I/O modules must be considered.

The controller can connect up to 32 I/O modules, including digital input and output, analog input and output and special function modules.

3L20(DEI Status	- System -	Settings -	I/O Module - Logout				
IO Slot	Module Name	Module Type	Channel Number	I/O Module EtherneviP Address	24V Address- State	Soft Version	IO Status	Channel Status
1	M1081	DI	8	IW0-0,IX0-0	9001-Power On	29	Normal	Channel Statu s
2	M2082	DO	8	QW0-0,QX0-0	9002-Power On	29	Normal	Channel Statu s
3	M1161	DI	16	IW0-1,IX1-2	9003-Power On	29	Normal	Channel Statu s
4	M2162	DO	16	QW0-1,QX1-2	9004-Power On	29	Normal	Channel Statu s
5	M3041	AI	4	IW1-5,IX3-10	9005-Power On	29	Normal	Channel Statu s
6	M3043	AI	4	IW5-9,IX11-18	9006-Power On	29	Normal	Channel Statu s
7	M3046	AI	4	IW9-13,IX19-26	9007-Power On	29	Normal	Channel Statu s
В	M4041	AO	4	QW1-5,QX3-10	9008-Power On	29	Normal	Channel Statu s
Э	M4043	AO	4	QW5-9,QX11-18	9009-Power On	29	Normal	Channel Statu s
10	M5021	RTD	2	IW13-15,IX27-30	9010-Power On	29	Normal	Channel Statu s
11	M5023	RTD	2	IW15-17,IX31-34	9011-Power On	29	Normal	Channel Statu s
12	M5048	RTD	4	IW17-21,IX35-42	9012-Power On	29	Normal	Channel Statu s

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BL203 serial module not support M602X, and M7011 and M8011 are not involved in 40 Shenzhen Beilai Technology Co., Ltd V1.2



the calculation.

I/O Model	T>O(Byte)	O>T(Byte)
M1081	1	
M1082	1	
M1161	2	
M1162	2	
M3041	8	
M3043	8	
M3044	8	
M3046	8	
M5021	4	
M5022	4	
M5023	4	
M5024	4	
M5048	8	
M2081		1
M2082		1
M2161		2
M2162		2
M2044		1
M4041		8
M4043		8
M4046		8

6 BL203 Communication Example

6.1 Omron NX1P2 and BL203 Communication

1. Preparation: Assembly of the module, wiring reference chapter 3 installation, chapter 4 connection.

2. Open the Sysmac Studio software, click New Project, select NX1P2-9024DT, click Create.



Sysmac Studio (32bit)		20 <u>-</u> 20		×
		_	_	
Offline Open Project Open Project Export Conline Connect to Device Version Control Version Control License License License	Project Properties Project name Author T Comment Type Standard Project Select Device Category Controller Device NX1P2 - 9024DT • Version 1.40			

3. Click Configuration and Settings-Built-in EtherNet/IP Port Settings to modify the IP address.

BL203 - new_Controller_0 - Sysmac Studio	(32bit)	_	
<u>Eile Edit View Insert Project Controll</u>	er <u>S</u> imulation <u>T</u> ools <u>W</u> indow <u>H</u> elp		
	< 路前間部 20 1 元 1 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2		
Multiview Explorer 🚽 🖡	Built-in EtherNet/IP Port S 🗴	Toolbox	÷ û
new_Controller_0	TCP TCP/IP Settings	<search></search>	V N X
Configurations and Setup			
EtherCAT CPU/Expansion Racks	FIP Address Fixed setting		
	IP address 192.1681.21_		
L Coperation Settings	Obtain from BOOTP server.		
Built-in EtherNet/IP Port Settings	FIP Fix at the IP address obtained from BOOTP server. Default Gateway		
L 🖾 Option Board Settings	Default gateway		
Memory Settings Motion Control Setup	□TP ▼ DNS		
 	DNS O Do not use Use Priority DNS server		
Task Settings	Snine Domain name Domain name		
Data Trace Settings Programming	▼ Host Name - IP Address		
V December 2005	Snmp Host Name IP Address		
V III Program0			
L 쿱· Section0 L 常 Functions	Reset all to default.		
L 😹 Function Blocks	Build • • • • ×		
 Data Tasks 	🗴 Ulterons 🛕 UlWernings		
i Filter	Output Build		

4. Set the computer IP and PLC in the same network segment. The PLC address is known to be 192.168.1.21



🔋 以太网 Status 🛛 🔧 👋	🔋 以太网 Properties 🛛 🗙	a Internet 协议版本 4 (TCP/IPv4) Properties
General	Networking Sharing	General
Connection IPv4 Connectivity: Internet IPv6 Connectivity: No network access	Connect using:	You can get IP settings assigned automatically if your network su this capability. Otherwise, you need to ask your network administ for the appropriate IP settings.
Media State: Enabled	This connection uses the following items:	Obtain an IP address automatically Oute the following IP address:
Speed: 1.0 Gbps Details Activity Sent — Received Bytes: 312,792,011 6,706,437,713 Properties Diagnose Diagnose	✓ Marcast 网络客户端 ✓ Marcast 网络客户端 ✓ 「Marcast 网络客户端 ✓ 「WauBan NDISS Bridged Networking Driver ✓ VauBan NDISS Bridged Networking Driver ✓ VauBan NDISS Bridged Networking Driver ✓ Coss 動揺包计分程序 ✓ a linemet 协议版本 4 (TCP/IPv4) ✓ ✓ Install	IP address: 192 . 168 . 1 . 58 Subnet mask: 255 . 255 . 0 Default gateway: 192 . 168 . 1 . 1 Obtain DNS server address automatically @Use the following DNS server addresses: Preferred DNS server: 192 . 168 . 1 . 1 Alternative DNS server: Ukaldate settings upon exit Advar
Close	OK Cancel	OK

5. Viewing the size of the input/output data area for Ethernet/IP communication, both the T-->O and O-->T Byte sizes, can be viewed through the web page configuration or calculated, refer to 5.7.4 Web Configuration. The T-->O size is known to be 40 Bytes and the O-->T size is known to be 19 Bytes through the web configuration.

BL200	EI Status +	System +	Settings -	I/O Module 👻 Logout				
2	M2082	DO	8	QWU-0,QX1-1	9002-Power On	29	Normal	S
3	M2081	DO	8	QW1-1,QX2-2	9003-Power On	29	Normal	Channel Statu s
4	M1081	DI	8	IW0-0,IX0-0	9004-Power On	29	Normal	Channel Statu s
5	M1081	DI	8	IW0-0,IX1-1	9005-Power On	29	Normal	Channel Statu s
6	M1082	DI	8	IW1-1,IX2-2	9006-Power On	29	Normal	Channel Statu s
7	M1082	DI	8	IW1-1,IX3-3	9007-Power On	29	Normal	Channel Statu s
8	M3046	AI	4	IW2-5,IX4-11	9008-Power On	29	Normal	Channel Statu s
9	M3046	AI	4	IW6-9,IX12-19	9009-Power On	29	Normal	Channel Statu S
10	M4041	AO	4	QW1-5,QX3-10	9010-Power On	29	Normal	Channel Statu s
11	M4043	AO	4	QW5-9 QX11-18	9011-Power On	29	Normal	Channel Statu s
12	M5021	RTD	2	IW10-11,IX20-23	9012-Power On	29	Normal	Channel Statu s
13	M5022	RTD	2	IW12-13,IX24-27	9013-Power On	29	Normal	Channel Statu s
14	M5023	RTD	2	IW14-15,IX28-31	9014-Power On	29	Normal	Channel Statu s
15	M5048	RTD	4	IW16-19 <mark>IX32-39</mark>	9015-Power On	29	Normal	Channel Statu s



6. Click "Global Variables", according to the configuration of the web page to find out the size of T-->O is 40 Bytes, the size of O-->T is 19 Bytes, create a new input variable input (ARRAY[0..39]OF Byte), a new output variable output (ARRAY[0..18]OF Byte), the network is open to select input and output respectively. Variables can also be created module by module, e.g., if the position of M1082 in slot 7 is IX3-3, the variable M1082 (ARRAY[3...3]OF Byte) is created.



7. Click "Tools", click "Ethernet/IP Connection Setting". Double-click the PLC in the pop-up window to enter the built-in Ethernet/IP port setting connection setting interface.



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8. Click "Registration All", select the input tag input and output tag output, and click "Register".



9. In the right toolbox, click the right mouse button, the pop-up box click to show the EDS library, the first time you use is required to install the BL203 EDS file.





10. After the installation is completed, in the toolbox box, click "+" to add a new BL203 coupler. Select "BL200EI" in the drop-down box of the model name item, select "2" for the version, fill in the coupler address according to the specifications, and refer to 5.3.3 IP address selector switch for the IP address description, and then click "Add".





11. Click the connection icon, click "+", select 192.168.1.253 BL200EI as the target device, select "Start Variable", input tag "input", output tag "output", in "Target Variable", input "100", output "150", the target byte size and the starting variable byte size are the same.



📓 BL203 - new_Controller_0 - Sysmac Studio (32bit)	— c	- X
Eile Edit View Insert Project Controller Simulation Iools Window Help		
X ● 毎 首 ゥ ぐ 酉 一寺 ㅅ 益 扇 扇 治 島 (衣) ▲ ≫ お 용 寺 雪 〇 입 양 」 江 鸟 鸟 鸟		
White Explore: Image: Section Sector Image: Sector Image: Sector Imag	Toolbox Target Device 192163.1.253 8 Toolbox Toolbox 192163.1.253 8 Toolbox 192163.1.253 8 Toolbox 192163.1.255 100000000000000000000000000000000000	ze [Byte]
1 Filter 🕜 Output Build	Import Tag Set	

12. Click on Controller - Online, or click on A settings to the PLC. Click Yes - Yes - OK in the pop-up window, and click "Transfer to Controller" to download the configuration to the PLC for the internal use of Ethernet/IP configuration.





BL203 - new_Controller_0 - Sysmac Studio	(32bit)										ΠX
<u>File Edit View Insert Project Control</u>	ler <u>S</u> imulation	<u>T</u> ools <u>N</u>	(indow <u>H</u> elj	р							
X C G G S C G B	< X 55 55	1 <i>ii</i> A	9 R	A X	63 🛃 🖡 1	• ○ º₂ ₫	P 🗆 Q	Q 12	-		
Multiview Explorer	EIP ^S Built-in Ethe	rNet/IP Port :	s 🗺 Glo nnection	ibal Variables	EtherNet/IP Devi	e List Built-in	EtherNet/IPecti	ion Se X		Toolbox Target Device	- 1 53 BL200EI Re
	• [8	Connectio Connectior Target 92.168.1.253	n ns/Max: 2 / 32 Device BL200El Rev	Connection N default_001	Exlusive Owner	out/Out Target ' out 100 htput 150	Variable Size [8 40 19	tyte] (Originator Variab input output	le Size [Byte] Connection 40 Multi-cast co 19 Point to Point	12 + 1	
Lis Operation Sectings Lill Built-in therMet/IP Port Settings Lill Built-in therMet/IP Port Settings Lill Memory Settings I Memory Settings		4]							Variable Name	Size (Byte)
 ▼ ■ POUs ▼ ■ Programs ▼ ■ Program0 L ■ Section0 L ■ Functions L ■ Functions Blocks ▼ ■ Data 		Device E Restar	Bandwidth t		C	ransfer to Control	ller Transfer	from Controller	Return All to Default Compare	Import Tag Set	
L 58 Data Types Lien Global Variables ► fa Tasks	Watch (Project Device na new_Controlle new_Controlle new_Controlle new_Controlle new_Controlle)1 ime 20 20 20 20 20 20 20 20	N input[039] output[018 input[2] input[16] input[17]	ame	I Online value 10 10 A4 62	H Modify	Comment	ARRAY[0.39] OF ARRAY[0.39] OF ARRAY[0.18] OF byte byte byte	I Display format I Hexadecim: Hexadecim: Hexadecim:	Controller Status	• 1 192.168.1.21 RUN mode
🖪 Filter 🕑	Output Build	Watch (Pro	oject)1								

13. Click "View" - "Watch Tab Page", add input and output variables in the monitor window to monitor the data. In the monitoring window can monitor all the data such as input[0..39], you can also monitor the data of a separate module such as input[2], the name here is the global variable created when the variable. Monitoring input[2] is shown as 10 in hexadecimal and 00010000 in binary, which means that the 5th path is closed and the other 7 paths are open. Monitor input[16] and input[17] combined 16-bit integer is "23039", according to the mapping relationship between AI and AO, refer to 5.4 Process Data Definition, the value of AI is "3.52V".





BL200EI	Status -	System -	Settings -	I/O Module -
---------	----------	----------	------------	--------------

IO status

		LIAddie	55		Va	alue	
		IX2.0			O	pen	
		IX2.1			Ol	pen	
		IX2.2			Ol	pen	
		IX2.3			O	pen	
		IX2.4			CI	ose	
		IX2.5			OI	pen	
		IX2.6			OI	pen	
		IX2.7			O	pen	
Back to Overvi	iew				Save & A	pply - Sav	ve Reset
BL200El O status	Status ≁ System ≁	Shenzhen Beilai Settings - I/O Module	Technology Co.,Ltd (V1.1.12) • Logout	/ 2023-10-19	_	_	
BL200EI O status O Slot:9,Mor Channels	Status - System - dule Type:Al,Mo El Address	Shenzhen Beilai Settings - I/O Module odule Name:M3046 Value	Technology Co.,Ltd (V1.1.12) Logout Mode	/ 2023-10-19	Min Value	Max Value	Offset(V)
BL200EI O status O Slot:9,Mo Channels	Status - System - dule Type:Al,Ma El Address IX12	Shenzhen Beilai Settings - I/O Module odule Name:M3046 Value 2.039062	Technology Co.,Ltd (V1.1.12) Co.,Ltd (V1.1.12) Mode Voltage -5-5V	/ 2023-10-19	Min Value	Max Value	Offset(V)
BL200EI O status O Slot:9,Mor Channels 1	Status - System - dule Type:Al,Ma El Address IX12 IX14	Shenzhen Beilai Settings - VO Module Odule Name:M3046 Value 2.039062 0.000000	Technology Co.,Ltd (V1.1.12) Co.,Ltd (V1.1.12) Logout Mode Voltage -5-5V Voltage -5-5V	/ 2023-10-19	Min Value	Max Value	Offset(V)
BL200EI O status O Slot:9,Mo Channels 1 2 3	Status - System - dule Type:AI,Ma El Address IX12 IX14 IX16	Shenzhen Beilai Settings - I/O Module odule Name:M3046 I/O Value 2.039062 0.000000 23019.914062	Technology Co.,Ltd (V1.1.12) Co.,Ltd (V1.1.12) Logout Mode Voltage -5-5V Voltage -5-5V Voltage -5-5V	/ 2023-10-19	Min Value	Max Value	Offset(V)

Logout

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14. DO data sent output [0] sent in the modified item fill in "10110011" keyboard press "Enter" key, you can sent binary, decimal, hexadecimal, pay attention to the display format.



Device name	l Name	Online value	Modify	Comment	Data type	I Display format
new_Controller_0	▶ input[039]				ARRAY[039] OF	
new_Controller_0	> output[018]				ARRAY[018] OF	
new_Controller_0	input[2]	0001 0000			byte	Binary 💌
new_Controller_0	input[16]	E1			byte	Hexadecim: 🔻
new_Controller_0	input[17]	59			byte	Hexadecim; 🔻
new_Controller_0	output[0]	1011 0011	10110011		byte	Binary 🔻
new_Controller_0	output[3]	00	1		byte	Hexadecim: 🔻
new_Controller_0	output[4]	00			byte	Hexadecim: 🔻
new_Controller_0	Input Name					
tout Build Wate	h (Project)1					

IO status

IO Slot:1,Module Type:DO,Module Name:M2082

Channels	El Address	Value	PowerOn Status		Open/Close
1	QX0.0	Close	Open	•	Open/Close
2	QX0.1	Close	Open	•	Open/Close
}	QX0.2	Open	Open	•	Open/Close
l	QX0.3	Open	Open	~	Open/Close
	QX0.4	Close	Open	~	Open/Close
	QX0.5	Close	Open	•	Open/Close
	QX0.6	Open	Open	•	Open/Close
	QX0.7	Close	Open	~	Open/Close

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15. Control AO data, control current output 10mA, according to the mapping relationship according to the AO refer to 5.4 process data definition, if the value is "12288", the high 8 bit is "30H", the low 8 bit is "0", then the low 8 bit output[3] sends "0", the high 8 bit output[4] sends "30H".

Watch (Project)1	aparapapapapapapapapapapapapapapapapapa	baaababaaaababaaaababaaababab	been and a second s	adadabbbbbbbbbbbbbbb	adadadadadadadadadadada	annananananan 🖕 🖡 🕽
Device name	Name	Online value	Modify	Comment	Data type	/ Display format
new_Controller_0	input[039]				ARRAY[039] OF	
new_Controller_0	> output[018]			Ĩ	ARRAY[018] OF	
new_Controller_0	input[2]	0001 0000			byte	Binary 🔻
new_Controller_0	input[16]	0D			byte	Hexadecim; 🔻
new_Controller_0	input[17]	5B			byte	Hexadecim: 🔻
new_Controller_0	output[0]	1011 0011			byte	Binary 🔽
new_Controller_0	output[3]	00 📶 🖬	00	1	byte	Hexadecim, 🔻
new_Controller_0	output[4]	30 高8位	30		byte	Hexadecim: 🔻
new_Controller_0	Input Name					
	1					



Channels	El Address	Value	Mode	Min Value	Max Value	Set Value
	QX3	12288.000000	Current 4-20mA	•		
	QX5	0.00000	Current 4-20mA	~		
	QX7	0.000000	Current 4-20mA	~		
1	QX9	0.000000	Current 4-20mA	•		

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7 Warranty

1) This equipment will be repaired free of charge for any material or quality problems within one year from the date of purchase.

2) This one-year warranty does not cover any product failure caused by man-made damage, improper operation, etc.

8 Technical Support

Shenzhen Beilai Technology Co., Ltd Website: https://www.bliiot.com